

FORM PTO-1390 (Modified) (REV 11-2000)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTORNEY'S DOCKET NUMBER <b>221608US0PCT</b>	
TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. 371				U.S. APPLICATION NO. (IF KNOWN, SEE 37 CFR <b>10/089067</b>	
INTERNATIONAL APPLICATION NO. <b>PCT/JP00/06957</b>		INTERNATIONAL FILING DATE <b>5 October 2000</b>		PRIORITY DATE CLAIMED <b>5 October 1999 (earliest)</b>	
TITLE OF INVENTION <b>SOLDERING FLUX, SOLDER PASTE AND METHOD OF SOLDERING</b>					
APPLICANT(S) FOR DO/EO/US <b>Minoru TAKAYA et al.</b>					
Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:					
<ol style="list-style-type: none"> <li>1. <input checked="" type="checkbox"/> This is a <b>FIRST</b> submission of items concerning a filing under 35 U.S.C. 371.</li> <li>2. <input type="checkbox"/> This is a <b>SECOND</b> or <b>SUBSEQUENT</b> submission of items concerning a filing under 35 U.S.C. 371.</li> <li>3. <input checked="" type="checkbox"/> This is an express request to begin national examination procedures (35 U.S.C. 371(f)). The submission must include items (5), (6), (9) and (24) indicated below.</li> <li>4. <input checked="" type="checkbox"/> The US has been elected by the expiration of 19 months from the priority date (Article 31).</li> <li>5. <input checked="" type="checkbox"/> A copy of the International Application as filed (35 U.S.C. 371 (c) (2)) <ol style="list-style-type: none"> <li>a. <input type="checkbox"/> is attached hereto (required only if not communicated by the International Bureau).</li> <li>b. <input checked="" type="checkbox"/> has been communicated by the International Bureau.</li> <li>c. <input type="checkbox"/> is not required, as the application was filed in the United States Receiving Office (RO/US).</li> </ol> </li> <li>6. <input checked="" type="checkbox"/> An English language translation of the International Application as filed (35 U.S.C. 371(c)(2)). <ol style="list-style-type: none"> <li>a. <input checked="" type="checkbox"/> is attached hereto.</li> <li>b. <input type="checkbox"/> has been previously submitted under 35 U.S.C. 154(d)(4).</li> </ol> </li> <li>7. <input checked="" type="checkbox"/> Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371 (c)(3)) <ol style="list-style-type: none"> <li>a. <input type="checkbox"/> are attached hereto (required only if not communicated by the International Bureau).</li> <li>b. <input type="checkbox"/> have been communicated by the International Bureau.</li> <li>c. <input type="checkbox"/> have not been made; however, the time limit for making such amendments has NOT expired.</li> <li>d. <input checked="" type="checkbox"/> have not been made and will not be made.</li> </ol> </li> <li>8. <input type="checkbox"/> An English language translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).</li> <li>9. <input checked="" type="checkbox"/> An oath or declaration of the inventor(s) (35 U.S.C. 371 (c)(4)).</li> <li>10. <input checked="" type="checkbox"/> An English language translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371 (c)(5)).</li> <li>11. <input type="checkbox"/> A copy of the International Preliminary Examination Report (PCT/IPEA/409).</li> <li>12. <input type="checkbox"/> A copy of the International Search Report (PCT/ISA/210).</li> </ol>					
<p><b>Items 13 to 20 below concern document(s) or information included:</b></p> <ol style="list-style-type: none"> <li>13. <input type="checkbox"/> An Information Disclosure Statement under 37 CFR 1.97 and 1.98.</li> <li>14. <input checked="" type="checkbox"/> An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.</li> <li>15. <input type="checkbox"/> A <b>FIRST</b> preliminary amendment.</li> <li>16. <input type="checkbox"/> A <b>SECOND</b> or <b>SUBSEQUENT</b> preliminary amendment.</li> <li>17. <input type="checkbox"/> A substitute specification.</li> <li>18. <input type="checkbox"/> A change of power of attorney and/or address letter.</li> <li>19. <input type="checkbox"/> A computer-readable form of the sequence listing in accordance with PCT Rule 13ter.2 and 35 U.S.C. 1.821 - 1.825.</li> <li>20. <input type="checkbox"/> A second copy of the published international application under 35 U.S.C. 154(d)(4).</li> <li>21. <input type="checkbox"/> A second copy of the English language translation of the international application under 35 U.S.C. 154(d)(4).</li> <li>22. <input type="checkbox"/> Certificate of Mailing by Express Mail</li> <li>23. <input checked="" type="checkbox"/> Other items or information:</li> </ol> <p><b>Notice of Priority/ PCT/IB/304</b>  <b>Drawings (22 sheets)/ PCT/IB/308</b>  <b>Article 34 Amendment (pages 54-56)</b></p>					

U.S. APPLICATION NO. (IF KNOWN, SEE 37 CFR <b>10/089067</b>		INTERNATIONAL APPLICATION NO. <b>PCT/JP00/06957</b>		ATTORNEY'S DOCKET NUMBER <b>221608US0PCT</b>	
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24. The following fees are submitted:

BASIC NATIONAL FEE ( 37 CFR 1.492 (a) (1) - (5)) :				CALCULATIONS PTO USE ONLY	
<input type="checkbox"/> Neither international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO and International Search Report not prepared by the EPO or JPO .....	<b>\$1040.00</b>				
<input checked="" type="checkbox"/> International preliminary examination fee (37 CFR 1.482) not paid to USPTO but International Search Report prepared by the EPO or JPO .....	<b>\$890.00</b>				
<input type="checkbox"/> International preliminary examination fee (37 CFR 1.482) not paid to USPTO but international search fee (37 CFR 1.445(a)(2)) paid to USPTO .....	<b>\$740.00</b>				
<input type="checkbox"/> International preliminary examination fee (37 CFR 1.482) paid to USPTO but all claims did not satisfy provisions of PCT Article 33(1)-(4) .....	<b>\$710.00</b>				
<input type="checkbox"/> International preliminary examination fee (37 CFR 1.482) paid to USPTO and all claims satisfied provisions of PCT Article 33(1)-(4) .....	<b>\$100.00</b>				
<b>ENTER APPROPRIATE BASIC FEE AMOUNT =</b>				<b>\$890.00</b>	
Surcharge of <b>\$130.00</b> for furnishing the oath or declaration later than months from the earliest claimed priority date (37 CFR 1.492 (e)). <input type="checkbox"/> 20 <input type="checkbox"/> 30				<b>\$0.00</b>	
CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE		
Total claims	49 - 20 =	29	x \$18.00	<b>\$522.00</b>	
Independent claims	7 - 3 =	4	x \$84.00	<b>\$336.00</b>	
Multiple Dependent Claims (check if applicable). <input type="checkbox"/>				<b>\$0.00</b>	
<b>TOTAL OF ABOVE CALCULATIONS =</b>				<b>\$1,748.00</b>	
<input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27). The fees indicated above are reduced by 1/2.				<b>\$0.00</b>	
<b>SUBTOTAL =</b>				<b>\$1,748.00</b>	
Processing fee of <b>\$130.00</b> for furnishing the English translation later than months from the earliest claimed priority date (37 CFR 1.492 (f)). <input type="checkbox"/> 20 <input type="checkbox"/> 30				<b>\$0.00</b>	
<b>TOTAL NATIONAL FEE =</b>				<b>\$1,748.00</b>	
Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31) (check if applicable). <input checked="" type="checkbox"/>				<b>\$40.00</b>	
<b>TOTAL FEES ENCLOSED =</b>				<b>\$1,788.00</b>	
				Amount to be: refunded	\$
				charged	\$

a. ☒ A check in the amount of **\$1,788.00** to cover the above fees is enclosed.


b. ☐ Please charge my Deposit Account No. \_\_\_\_\_ in the amount of \_\_\_\_\_ to cover the above fees. A duplicate copy of this sheet is enclosed.

c. ☒ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. **15-0030** A duplicate copy of this sheet is enclosed.

d. ☐ Fees are to be charged to a credit card. **WARNING:** Information on this form may become public. **Credit card information should not be included on this form.** Provide credit card information and authorization on PTO-2038.

**NOTE:** Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b)) must be filed and granted to restore the application to pending status.

SEND ALL CORRESPONDENCE TO:



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\_\_\_\_\_  
SIGNATURE

William F. O'Connell  
NAME

Registration Number 30,991  
24,618

REGISTRATION NUMBER

April 5, 2002  
DATE

221608US-0 PCT

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF: :  
MINORU TAKAYA ET AL : ATTN: APPLICATION DIVISION  
SERIAL NO: NEW U.S. PCT APPLN :  
(BASED ON PCT/JP00/06957)  
FILED: HEREWITH :  
FOR: SOLDERING FLUX, SOLDER  
PASTE AND METHOD OF  
SOLDERING

PRELIMINARY AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

Prior to examination on the merits, please amend the above-identified application as follows.

IN THE CLAIMS

Please cancel Claims 1-20.

Please add the following new claims.

21. (New) A soldering flux comprising:

an adhesive resin and a hardening agent, wherein:

said hardening agent includes at least two types of carboxylic acids having melting points that are different from each other.

22. (New) The soldering flux of claim 21, assuming a liquid form.
23. (New) The soldering flux of claim 21, assuming a paste form.
24. (New) The soldering flux of claim 21, wherein:  
said adhesive resin includes a thermo-setting resin.
25. (New) The soldering flux of claim 24, wherein:  
said thermo-setting resin is selected from the group consisting of epoxy resin, phenol resin, polyimide resin, silicon resin, modified resin and acrylic resin.
26. (New) The soldering flux of claim 21, wherein:  
one of said two types of carboxylic acids is adipic acid and another of said two types of carboxylic acid is pimelic acid.
27. (New) The soldering flux of claim 26, represented by the formula:
- $$a : b = (85 : 15) \text{ to } (95 : 5)$$
- wherein 'a' is content (mass%) of said adipic acid and 'b' is content (mass%) of said pimelic acid.
28. (New) The soldering flux of claim 21, wherein:  
one of said two types of carboxylic acids is adipic acid and another of said two types of carboxylic acids is succinic acid.
29. (New) The soldering flux of claim 28, represented by the formula:
- $$a : c = (95 : 5) \text{ to } (25 : 75)$$
- wherein 'a' is content (mass%) of said adipic acid and 'c' is content (mass%) of said succinic acid.
30. (New) A soldering paste comprising:  
soldering powder; and

a soldering flux mixed with said soldering powder, wherein:

said soldering flux comprises an adhesive resin and a hardening agent, and said hardening agent includes at least two types of carboxylic acids having melting points that are different from each other.

31. (New) The soldering paste of claim 30, wherein:

said adhesive resin includes a thermo-setting resin.

32. (New) The soldering paste of claim 31, wherein:

said thermo-setting resin is selected from the group consisting of epoxy resin, phenol resin, polyimide resin, silicon resin, modified resin and acrylic resin.

33. (New) The soldering paste of claim 30, wherein:

one of said two types of carboxylic acids is adipic acid and another of said two types of carboxylic acid is pimelic acid.

34. (New) The soldering paste of claim 33, represented by the formula:

$$a : b = (85 : 15) \text{ to } (95 : 5)$$

wherein 'a' is content (mass%) of said adipic acid and 'b' is content (mass%) of said pimelic acid.

35. (New) The soldering paste of claim 30, wherein:

one of said two types of carboxylic acids is adipic acid and another of said two types of carboxylic acids is succinic acid.

36. (New) The soldering paste of claim 35, represented by the formula:

$$a : c = (95 : 5) \text{ to } (25 : 75)$$

wherein 'a' is content (mass%) of said adipic acid and 'c' is content (mass%) of said succinic acid.

37. (New) The soldering paste of claim 30, wherein:

said soldering powder includes a component selected from the group consisting of Sn, Cu, Ag, Sb, Pb, In, Zn and Bi.

38. (New) An electronic component device comprising:

a component mounting board;

an electronic component soldered onto said component mounting board; and

a soldering flux between said component mounting board and said electronic component to bond said component mounting board and said electronic component to each other, wherein:

said soldering flux comprises an adhesive resin and a hardening agent, and said hardening agent includes at least two types of carboxylic acids having melting points that are different from each other.

39. (New) The electronic component device of claim 38, wherein:

said soldering flux assumes a liquid form.

40. (New) The electronic component device of claim 38, wherein:

said soldering flux assumes a paste form.

41. (New) The electronic component device of claim 38, wherein:

said adhesive resin includes a thermo-setting resin.

42. (New) The electronic component device of claim 41, wherein:

said thermo-setting resin is selected from the group consisting of epoxy resin, phenol resin, polyimide resin, silicon resin, modified resin and acrylic resin.

43. (New) The electronic component device of claim 38, wherein:

one of said two types of carboxylic acids is adipic acid and another of said two types of carboxylic acid is pimelic acid.

44. (New) The electronic component device of claim 43, represented by the formula:

$$a : b = (85 : 15) \text{ to } (95 : 5)$$

wherein 'a' is content (mass%) of said adipic acid and 'b' is content (mass%) of said pimelic acid.

45. (New) The electronic component device of claim 38, wherein:

one of said two types of carboxylic acids is adipic acid and another of said two types of carboxylic acids is succinic acid.

46. (New) The electronic component device of claim 35, represented by the formula:

$$a : c = (95 : 5) \text{ to } (25 : 75)$$

wherein 'a' is content (mass%) of said adipic acid and 'c' is content (mass%) of said succinic acid.

47. (New) An electronic circuit module comprising:

a chip mounting board;

a semiconductor chip having at least one semiconductor element soldered onto said chip mounting board; and

a soldering flux between said chip mounting board and said semiconductor chip to bond said chip mounting board and said semiconductor chip to each other, wherein:

said soldering flux comprises an adhesive resin and a hardening agent, and said hardening agent includes at least two types of carboxylic acids having melting points that are different from each other.

48. (New) The electronic circuit module of claim 47, wherein:

said soldering flux assumes a liquid form.

49. (New) The electronic circuit module of claim 47, wherein:

said soldering flux assumes a paste form.

50. (New) The electronic circuit module of claim 49, wherein:

said adhesive resin includes a thermo-setting resin.

51. (New) The electronic circuit module of claim 50, wherein:

said thermo-setting resin is selected from the group consisting of epoxy resin, phenol resin, polyimide resin, silicon resin, modified resin and acrylic resin.

52. (New) The electronic circuit module of claim 47, wherein:

one of said two types of carboxylic acids is adipic acid and another of said two types of carboxylic acid is pimelic acid.

53. (New) The electronic circuit module of claim 52, represented by the formula:

$$a : b = (85 : 15) \text{ to } (95 : 5)$$

wherein 'a' is content (mass%) of said adipic acid and 'b' is content (mass%) of said pimelic acid.

54. (New) The electronic circuit module of claim 47, wherein:

one of said two types of carboxylic acids is adipic acid and another of said two types of carboxylic acids is succinic acid.

55. (New) The electronic circuit module of claim 54, represented by the formula:

$$a : c = (95 : 5) \text{ to } (25 : 75)$$

wherein 'a' is content (mass%) of said adipic acid and 'c' is content (mass%) of said succinic acid.

56. (New) An electronic circuit apparatus comprising:



a motherboard;  
an electronic circuit module soldered onto said motherboard; and  
a soldering flux between said motherboard and said electronic circuit module to bond  
said motherboard and said electronic circuit module to each other, wherein:

said soldering flux comprises an adhesive resin and a hardening agent, and said  
hardening agent includes at least two types of carboxylic acids having melting points that are  
different from each other.

57. (New) The electronic circuit apparatus of claim 56, wherein:  
said soldering flux assumes a liquid form.

58. (New) The electronic circuit apparatus of claim 56, wherein:  
said soldering flux assumes a paste form.

59. The electronic circuit apparatus of claim 56, wherein:  
said adhesive resin includes a thermo-setting resin.

60. (New) The electronic circuit apparatus of claim 59, wherein:  
said thermo-setting resin is selected from the group consisting of epoxy resin, phenol  
resin, polyimide resin, silicon resin, modified resin and acrylic resin.

61. (New) The electronic circuit apparatus of claim 56, wherein:  
one of said two types of carboxylic acids is adipic acid and another of said two types  
of carboxylic acid is pimelic acid.

62. (New) The electronic circuit apparatus of claim 61, represented by the formula:

$$a : b = (85 : 15) \text{ to } (95 : 5)$$

wherein 'a' is content (mass%) of said adipic acid and 'b' is content (mass%) of said  
pimelic acid.

63. (New) The electronic circuit apparatus of claim 56, wherein:  
one of said two types of carboxylic acids is adipic acid and another of said two types of carboxylic acids is succinic acid.

64. (New) The electronic circuit apparatus of claim 63, represented by the formula:

$$a : c = (95 : 5) \text{ to } (25 : 75)$$

wherein 'a' is content (mass%) of said adipic acid and 'c' is content (mass%) of said succinic acid.

65. (New) A soldering method implemented by using a flux comprising an adhesive resin and a hardening agent, wherein:

said hardening agent includes at least two types of carboxylic acids having melting points that are different from each other.

66. (New) The soldering method of claim 65, implemented to solder any one of an electronic component, an electronic circuit module and a semiconductor chip onto a board.

67. (New) A soldering method implemented by using a soldering paste comprising soldering powder mixed with a soldering flux, wherein:

said soldering flux comprises an adhesive resin and a hardening agent, and said hardening agent includes at least two types of carboxylic acids having melting points that are different from each other.

68. (New) The soldering method of claim 67, implemented to solder any one of an electronic component, an electronic circuit module and a semiconductor chip onto a board.

69. (New) The soldering method of claim 67, including the steps of:  
executing a soldering process at one surface of said board by using said soldering paste, and

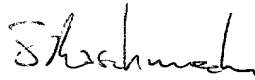
soldering an electronic component onto another surface of said board with a solder different from said soldering paste.

REMARKS

Claims 21-69 are active in the present application. Claims 1-20 have been canceled. Claims 20-69 are new claims. Support for the new claims is found in the original claims. No new matter is added. An action on the merits and allowance of claims is solicited.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.



Norman F. Oblon  
Attorney of Record  
Registration No. 24,618

Stefan U. Koschmieder  
Registration No. 50,238



**22850**

Tel.: (703) 413-3000

Fax: (703) 413-2220

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**Marked-Up Copy**

Serial No: \_\_\_\_\_

Amendment Filed on: \_\_\_\_\_

4-5-2002

IN THE CLAIMS

--Claims 1-20 (Canceled).

Claims 21-69 (New).--

DOCKET NO.: 221608US0PCT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION OF: Minoru TAKAYA, et al.

SERIAL NO.: NEW U.S. PCT APPLICATION

FILED: HERewith

INTERNATIONAL APPLICATION NO.: PCT/JP00/06957

INTERNATIONAL FILING DATE: October 5, 2000

FOR: SOLDERING FLUX, SOLDER PASTE AND METHOD OF SOLDERING

**REQUEST FOR PRIORITY UNDER 35 U.S.C. 119**  
**AND THE INTERNATIONAL CONVENTION**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In the matter of the above-identified application for patent, notice is hereby given that the applicant claims as priority:

<b><u>COUNTRY</u></b>	<b><u>APPLICATION NO</u></b>	<b><u>DAY/MONTH/YEAR</u></b>
Japan	11-284859	05 October 1999
Japan	2000-83001	23 March 2000
Japan	2000-218046	18 July 2000

Certified copies of the corresponding Convention application(s) were submitted to the International Bureau in PCT Application No. PCT/JP00/06957. Receipt of the certified copy(s) by the International Bureau in a timely manner under PCT Rule 17.1(a) has been acknowledged as evidenced by the attached PCT/IB/304.

Respectfully submitted,  
OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.



William E. Beaumont

Norman F. Oblon Registration Number 30,995  
Attorney of Record  
Registration No. 24,618  
Surinder Sachar  
Registration No. 34,423



22850

(703) 413-3000  
Fax No. (703) 413-2220  
(OSMMN 1/97)

## SOLDERING FLUX, SOLDER PASTE AND METHOD OF SOLDERING

### BACKGROUND OF THE INVENTION

#### 1. Field of the Invention

5       The present invention relates to a soldering flux, soldering paste, an electronic component device, an electronic circuit module, an electronic circuit apparatus and a soldering method.

#### 2. Description of the Prior Art

10       A flux is used to solder a component onto a component mounting board in the known art. The main function of the flux is to improve the wettability of the solder by removing the oxide film formed at a metal conductor provided at the component mounting board and also at the surface of the metal to be soldered at the component. Among fluxes used in the related art, those containing rosin as the primary constituent are the best known. Rosin contains a carboxylic acid such as abietic acid or levopimaric acid, and the carboxyl group acts to remove the oxide film at the surface of the metal to be soldered.

15       Various additives such as solvents, plasticizers and thixotropic agents are normally mixed into a flux to improve the printability and increase the retentive strength. For instance, Japanese Unexamined Patent Publication No. 1999-121915 discloses a flux, the viscosity of which is adjusted by adding alcohol.

20       Other types of fluxes include the RMA (halogen-free) type fluxes conforming to the MIL Standard. This type of flux eliminates the necessity of washing the flux and the like after the reflow process.

25

Since none of the fluxes described above are used to bond the soldered component following the soldering process, the solder joint is achieved through fusion joining of the metal to be soldered. Accordingly, the degree of strength with which the metals to be soldered are bonded with each other is determined by the size of the solder joint area.

As components become further miniaturized to keep pace with higher density mounting pursued in various types of electronics devices, the pitch representing the distance between the individual components is becoming smaller, which, in turn, has resulted in a drastically reduced solder joint area. Thus, it is difficult to ensure a sufficient soldering strength even today. Since it is expected that higher density at the mounting board, further miniaturization of components and a smaller pitch between individual components will be pursued with increasing vigor in the future, it will become even more difficult to support this technical trend with the means for assuring the bonding strength in the related art that achieves the required bonding strength simply by allowing the solder joint to be formed over a large enough area.

The required solder joint strength is normally assured by forming a solder fillet portion and thus, increasing the solder joint area where a terminal of the component and a conductor (a land or a solder bump) on the component mounting board become soldered to each other. However, since the bonding area of the fillet portion itself is bound to be reduced in high-density mounting, the fillet portion cannot readily contribute to an increase in the bonding strength.

In addition, when manufacturing various types of electronic circuit modules, for instance, a component mounting board that allows components to be mounted on both sides is often utilized so that after

soldering components in a furnace, onto one surface of the component mounting board with a high-temperature solder, components are mounted onto the other surface and the board undergoes a soldering process in the furnace again. This means that when soldering the components onto the other surface of the component mounting board, it is necessary to use a low-temperature solder having a lower melting point than the high-temperature solder used on the first side. In the prior art, the melting point of a solder is usually adjusted in conformance to Pb content.

At the same time, in response to the demand for solders that do not contain Pb (hereinafter referred to Pb-free solders) by the advocates of global environment conservation, great effort has been invested in developing solders with Pb-free composition in recent years. However, no Pb-free solder composition with a high melting point comparable to that of a high-temperature solder in the prior art has been put into practical use to date. The main obstacle is that the melting point of Pb-free solder itself is approximately 220°C that is higher than the melting point of a eutectic solder by as much as 40°C and thus, no effective alternative to Pb has been discovered. Consequently, the issue of insufficient difference between the melting points of the solders used on the two sides of a double-sided installation type component mounting board is yet to be addressed, and the problem of components becoming lifted off or falling off when they are mounted onto the component mounting board is still common.

Furthermore, when manufacturing an electronic circuit module using semiconductor chips, an additional work process is implemented after the semiconductor chips are soldered onto the chip mounting board.



A sealant is applied onto the bonding interface to firmly bond the semiconductor chips to the chip mounting board with the sealant.

However, if there is any residual flux present when the sealant is injected, the flux prevents the sealant from reaching the interface between the semiconductor chips and the board in sufficient quantity and thus, the sealant fails to achieve its full bonding strength. Accordingly, a process of washing away the flux is added before injecting the sealant. Under normal circumstances, the flux washing process is implemented by using a volatile organic solvent in a plurality of separate steps. However, stricter regulations are imposed today on the use of volatile organic solvents for environmental protection, and thus, the flux washing process has become an onerous process from the viewpoints of both cost effectiveness and environmental protection.

Moreover, when a combination of a ceramic substrate and an organic resin substrate is adopted to constitute the chip mounting board and the motherboard respectively to manufacture an electronic circuit apparatus by mounting an electronic circuit module obtained through the process described above onto the motherboard, the contraction stress tends to concentrate in the solder joint to result in cracks to occur readily in the solder joint to result in a reduction in the durability of the joints due to the difference between the coefficients of thermal expansion of the ceramic board and the organic resin board, as proven in thermal shock tests and the like conducted after the electronic circuit module is mounted. While it would be desirable to inject a sealant in order to improve the strength of the solder joint for this reason, it would lead to a considerable increase in the production cost to implement a flux washing process and a sealant

injection process on the entire motherboard, and thus, the sealant is not injected in reality.

## SUMMARY OF THE INVENTION

It is an object of the present invention to provide a soldering flux,  
5 a soldering paste and a soldering method that achieve a sufficient degree of bonding strength while allowing higher mounting density, further miniaturization of components and a reduction in the pitch at which components are set.

It is a further object of the present invention to provide a  
10 soldering flux, a soldering paste and a soldering method that prevent components from becoming lifted off or falling off with a high degree of reliability without having to ensure a sufficient difference between the melting points of the solders used at the two surfaces of a double-sided installation type component mounting board.

It is a still further object of the present invention to provide an  
15 electronic component device, an electronic circuit module and an electronic circuit apparatus that may be manufactured at low production costs without requiring a flux washing process.

It is a still further object of the present invention to provide an  
20 electronic component device, an electronic circuit module and an electronic circuit apparatus achieving a high degree of reliability, in that the durability of the solder joint is greatly increased compared to that in the prior art.

In order to achieve the objects described above, the soldering flux  
25 according to the present invention contains an adhesive resin and a hardening agent.

Since the flux according to the present invention contains an adhesive resin and a hardening agent, the adhesive resin can be used as an adhesive to secure a component to the component mounting board after the soldering process. As a result, the reliability of the solder joint is improved by preventing the component from peeling or falling off due to an impact force or thermal stress. This greatly differentiates the flux according to the present invention from the rosin flux in the prior art that does not act as an adhesive after the soldering process.

In addition, by using the flux according to the present invention, a sufficient bonding strength can be assured without having to form a fillet portion. Thus, since it is not necessary to provide an area for the formation of the fillet portion at a conductor for component connection (land) formed on the component mounting board, an improvement in the mounting density is achieved.

The adhesive resin contained in the flux according to the present invention may be a highly adhesive resin selected from numerous resin materials in conformance to temperature requirements. The selection of such a resin ensures that even when the standard eutactic solder is used on the second surface of the double-sided installation type component mounting board and the component mounting board is processed in a reflow furnace after components are soldered onto the first surface of the component mounting board by utilizing the flux according to the present invention, the components mounted at the first surface do not manifest such problems as shifting, the so-called Manhattan phenomenon (the raised component phenomenon) and falling. It goes without saying that the flux according to the present invention may be utilized in the soldering processes on both the first surface and the second surface.

The flux according to the present invention may be achieved in either a liquid form or a paste form. Such a flux can be applied onto the component mounting board or the like with ease through a means such as printing, dispenser application, spraying or brush application.

5 It is desirable to constitute the adhesive resin in the flux according to the present invention with a thermo-setting resin. More specifically, the thermo-setting resin may be constituted of at least one resin selected from epoxy resins, phenol resins, polyimide resins, silicon resins, modified resins and acrylic resins. A selection may be made from the resin materials  
10 listed above and the selected material(s) may be mixed at quantities set in conformance to bonding temperature range, target film hardness and the like.

The hardening agent must be capable of hardening the adhesive resin. In a desirable mode, the hardening agent contains a carboxylic acid.  
15 A hardening agent containing a carboxylic acid can be utilized as a flux for removing the oxide film formed at the surface of the metal to be soldered as well as an agent that hardens the thermo-setting resin.

The flux according to the present invention may contain a solvent, a plasticizer, a thixotropic agent and the like as well. The solvent is added  
20 to enable adjustment of setting temperature and setting speed of the adhesive resin and also to enable adjustment of the viscosity in conformance to the application mode. The plasticizer and the thixotropic agent, too, are added to enable adjustment of the viscosity in conformance to the mode of application. The quantities of the solvent, the plasticizer,  
25 the thixotropic agent and the like that are added are set in conformance to the specific purpose of use.

The flux according to the present invention may be achieved in the form of a micro-capsule having an adhesive resin, an organic acid that achieves a deoxidation effect, a carboxylic acid, a solvent and a hardening agent sealed therein.

5 In addition, the flux according to the present invention may be mixed with soldering powder to form a soldering paste. The soldering powder may be constituted of a substance selected from Sn, Cu, Ag, Sb, Pb, In, Zn and Bi. A Pb-free soldering paste can be achieved by using soldering powder other than Pb.

10 The present invention also discloses an electronic component device, an electronic circuit module and an electronic circuit apparatus achieved by using the flux described above. The electronic component device according to the present invention includes at least one electronic component, a component mounting board and a soldering flux. The  
15 electronic component is soldered onto the component mounting board. The soldering flux, that contains an adhesive resin and a hardening agent, is present between the electronic component and the component mounting board to bond the electronic component and the component mounting board to each other.

20 The soldering flux present between the electronic component and the component mounting board, contains the adhesive resin and the hardening agent, functions as an adhesive. This soldering flux does not need to be washed and can be directly utilized as an adhesive soldering flux. As a result, an electronic component device can be manufactured at  
25 low production cost without having to implement a flux washing process. Furthermore, since the soldering flux containing the adhesive resin and the hardening agent functions as an adhesive, a highly reliable electronic

component device in which the solder joint durability is greatly increased compared to the prior art is achieved.

The electronic circuit module according to the present invention includes a semiconductor chip, a chip mounting board and a soldering flux. The semiconductor chip includes at least one semiconductor element and is soldered onto the chip mounting board. The soldering flux that contains an adhesive resin and a hardening agent is present between the semiconductor chip and the chip mounting board to bond the semiconductor chip and the chip mounting board to each other.

The soldering flux present between the semiconductor chip and the chip mounting board, that contains the adhesive resin and the hardening agent, functions as an adhesive. This soldering flux does not need to be washed and can be directly utilized as an adhesive soldering flux. As a result, an electronic circuit module can be manufactured at low production cost without having to implement a flux washing process. Furthermore, since the soldering flux containing the adhesive resin and the hardening agent functions as an adhesive, a highly reliable electronic circuit module in that the solder joint durability is greatly increased compared to the prior art is achieved.

The electronic circuit apparatus according to the present invention includes an electronic circuit module, a motherboard and a soldering flux. The electronic circuit apparatus is soldered onto the motherboard. The soldering flux that contains an adhesive resin and a hardening agent is present between the semiconductor chip and the motherboard to bond them to each other. This soldering flux does not need to be washed and can be directly utilized as an adhesive soldering flux. As a result, an electronic circuit apparatus can be manufactured at low production cost

without having to implement a flux washing process. Furthermore, since the soldering flux containing the adhesive resin and the hardening agent functions as an adhesive, a highly reliable electronic circuit apparatus in which the solder joint durability is greatly increased compared to the prior art is achieved.

The present invention further discloses a soldering method achieved by using the flux and the soldering paste described above.

### BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 presents partial cross-sectional views illustrating a chip component soldering method achieved by using the flux according to the present invention;

FIG. 2 presents a partial cross-sectional view illustrating the method adopted to conduct a component shear strength test to measure the strength against a shear force applied to a chip component soldered onto the component mounting board;

FIG. 3 presents the results of the component shear strength test illustrated in FIG. 2;

FIG. 4 presents partial cross-sectional views illustrating the component mounting board in detail and the soldering process implemented to solder a chip component onto the component mounting board by using a soldering paste containing the flux according to the present invention;

FIG. 5 presents partial cross-sectional views illustrating the component mounting board in detail and the soldering process implemented to solder a chip component onto the component mounting board by using a soldering paste containing a rosin flux in the prior art;

FIG. 6 presents the results of component shear strength tests conducted in conjunction with the soldering method according to the present invention illustrated in FIG. 4 and the soldering method in the prior art illustrated in FIG. 5;

FIG. 7 presents partial cross-sectional views illustrating a soldering method achieved by utilizing a soldering paste containing the flux according to the present invention;

FIG. 8 presents a cross-sectional view taken along line 8-8 in FIG. 7;

FIG. 9 presents partial cross-sectional views of a soldering method achieved by using a soldering paste containing a rosin flux in the prior art;

FIG. 10 presents a partial cross-sectional view taken along line 10-10 in FIG. 9;

FIG. 11 shows the relationship between the reflow temperature and the component shear strength achieved by soldering a chip component with the soldering paste according to the present invention and the equivalent relationship achieved by soldering a chip component with a soldering paste containing a rosin flux in the prior art;

FIG. 12 presents a partial cross-sectional view of the electronic circuit module according to the present invention;

FIG. 13 presents a partial cross-sectional view illustrating the soldering method adopted in the electronic circuit module shown in FIG. 12;

FIG. 14 presents a partial cross-sectional view of another example of the electronic circuit module according to the present invention;



FIG. 15 presents a partial cross-sectional view illustrating the soldering method adopted in the electronic circuit module shown in FIG. 14;

FIG. 16 is a graph of the results of RDC measurement;

FIG. 17 presents a partial cross-sectional view of the electronic circuit apparatus according to the present invention;

FIG. 18 presents a partial cross-sectional view illustrating the soldering method adopted in the electronic circuit apparatus shown in FIG. 17;

FIG. 19 presents a partial cross-sectional view of another example of the electronic circuit apparatus according to the present invention;

FIG. 20 presents a partial cross-sectional view illustrating the soldering method adopted in the electronic circuit apparatus shown in FIG. 19;

FIG. 21 is a graph of the results of RDC measurement;

FIG. 22 presents a partial cross-sectional view illustrating the process of soldering an electronic circuit module onto the motherboard;

FIG. 23 presents a partial cross-sectional view illustrating the step implemented following the step shown in FIG. 22;

FIG. 24 presents a partial cross-sectional view illustrating the step implemented following the step shown in FIG. 23;

FIG. 25 presents a partial cross-sectional view illustrating the step implemented following the step shown in FIG. 24;

FIG. 26 presents a partial cross-sectional view illustrating the process of removing an electronic circuit module from the motherboard;

FIG. 27 presents a partial cross-sectional view illustrating the step implemented following the step shown in FIG. 26;

FIG. 28 shows the relationships between the reflow temperature and the component shear strength observed when capacitors are soldered by using soldering paste samples 21 and 22; and

FIG. 29 shows the relationships between the reflow temperature and the component shear strength observed when capacitors are soldered by using soldering paste samples 50 to 54.

#### BRIEF DESCRIPTION OF THE PREFERRED EMBODIMENTS

1. Flux, soldering paste and application thereof in electronic component device

##### Embodiment 1

Bisphenol A was used to constitute the thermo-setting resin and an anhydride of carboxylic acid was used as a hardening agent. The mixing ratio of the thermo-setting resin and the hardening agent was set at 1:1 by weight. In addition, a required degree of viscosity was assured by mixing in small quantities of a solvent and a thixotropic agent.

A flux 3 prepared by adopting the composition described above was applied (see FIG. 1b) onto a component mounting board 1 (see FIG. 1a) upon which solder bumps 21 and 22 had been deposited in advance. Next, as illustrated and FIG. 1c, an electronic component 4 achieved as a 1mm (length)  $\times$  0.5mm (width) chip was mounted. The electronic component 4 having terminal electrodes 41 and 42 provided at the two ends of a base body 40 to face opposite each other was mounted onto the component mounting board 1 so as to set the terminal electrodes 41 and 42 on the solder bumps 21 and 22 respectively. The component mounting board 1 having been mounted with the electronic component 4 was

processed in a reflow furnace to solder-bond the terminal electrodes 41 and 42 provided at the two ends of the base body 40 of the electronic component 4 to the solder bumps 21 and 22. Through this process, an electronic component device according to the present invention was obtained. The flux 3 that filled the gap formed between the electronic component 4 and the component mounting board 1 functioned as an adhesive soldering flux. The electronic component 4 and the electronic component device thus obtained was pushed in a lateral direction F1 as shown in FIG. 2 to measure the component shear strength.

#### Comparison example 1

For purposes of comparison, a component was mounted and soldered as shown in FIG. 1c by using a rosin flux in the prior art, and then, the component shear strength of the soldered component was measured through the testing method illustrated in FIG. 2.

FIG. 3 shows the results of the shear strength tests. As shown in FIG. 3, while the shear strength average achieved in comparison with embodiment 1, in which the rosin flux was used as in the prior art was approximately 800g, an approximate shear strength average of 1600g was achieved in embodiment 1 in which the flux according to the present invention was used.

The flux filling the gap formed between the electronic component 4 and the component mounting board 1 contains the adhesive resin and the hardening agent, and thus it can be used as an adhesive material. This flux (soldering flux) 3 does not need to be washed and can be directly utilized as an adhesive soldering flux. As a result, an electronic component device can be manufactured at low production cost without having to implement

a flux washing process. In addition, since the soldering flux constituted of the flux 3 that contains the adhesive resin and the hardening agent functions as an adhesive soldering flux, a highly reliable electronic component device achieving a far greater durability in the solder joints is obtained.

## Embodiment 2

A soldering paste was prepared by mixing the flux prepared in embodiment 1 with soldering powder. The quantity of the flux that was mixed relative to the quantity of the soldering powder was set at 10wt%. A chip component was soldered onto a component mounting board using this soldering paste. FIGS. 4a through 4c present partial cross-sectional views, showing the component mounting board in detail and the soldering process through which the chip component was soldered onto the component mounting board. The component mounting board 1 included two lands formed by sequentially laminating a Cu film 51 (52), an Ni film 61 (62) and an Au film 71 (72).

The soldering paste 81 (82) according to the present invention was applied onto each land at the component mounting board 1 (see FIG. 4a). The soldering paste 81 (82) was applied through a printing process implemented by using a metal mask having a 100 $\mu$ m thickness. The dimensions of the opening at the metal mask were set to 0.5mm  $\times$  0.3mm to match the dimensions of the lands on which an electronic component 4 was to be mounted.

Then, the electronic component 4 which was 1mm in length and 0.5mm in width was mounted onto the soldering paste 81 (82) (see FIG. 4b), and the assembly was processed in a reflow furnace to solder the

electronic component 4 onto the component mounting board 1 (see FIG. 4c). Through the process described above, an electronic component device according to the present invention was obtained.

Subsequently, the shear strength of the electronic component device was measured through the method illustrated in FIG. 2. In FIG. 4c, reference numeral 3 indicates the flux according to the present invention having been contained in the soldering paste 81 (82) and then formed fillets on the outer sides of the terminal electrodes 41 and 42.

#### Comparison example 2

For purposes of comparison, a soldering paste containing a rosin flux was used to solder a chip component onto a component mounting board as in the related art. The quantity of the rosin flux mixed to constitute the soldering paste relative to the quantity of soldering powder was set at 10wt%.

FIGS. 5a through 5c present partial cross-sectional views showing the component mounting board in detail and the soldering process through which the chip component was soldered onto the component mounting board. As shown in the figures, the component mounting board 1 included two lands formed by sequentially laminating a Cu film 51 (52), an Ni film 61 (62) and an Au film 71 (72). The soldering paste 91 (92) containing the rosin flux was applied onto the lands at the component mounting board 1 (see FIG. 5a).

Then, an electronic component 4 that was 1mm in length and 0.5mm in width was mounted onto the soldering paste 91 (92) containing the rosin flux (see FIG. 5b), and the assembly was processed in a reflow furnace to solder the electronic component 4 onto the component

mounting board 1 (see FIG. 5c). Subsequently, the shear strength of the electronic component device was measured through the method illustrated in FIG. 2.

FIG. 6 presents the results of the component shear strength test conducted on embodiment 2 and comparison example 2. As shown in the figure, while the shear strength average achieved in comparison example 2 was approximately 600g, an approximate average strength of 1500g was achieved in embodiment 2.

### Embodiment 3

A soldering paste was prepared by mixing the flux prepared in embodiment 1 with soldering powder. The quantity of the flux that was mixed relative to the quantity of the soldering powder was increased to 20 to 45wt%.

An electronic component 4 was soldered onto a component mounting board 1 using this soldering paste as illustrated in FIG. 7. To explain in more specific terms in reference to FIG. 7, the component mounting board 1 included two lands formed by sequentially laminating a Cu film 51 (52), an Ni film 61 (62) and an Au film 71 (72) (see FIG. 7a).

The soldering paste 81 (82) according to the present invention was applied onto each land at the component mounting board 1 (see FIG. 7a). The soldering paste 81 (82) was applied through a printing process implemented by using a metal mask having a 100 $\mu$ m thickness. The dimensions of the opening at the metal mask were set to 0.5mm X 0.3mm to match the dimensions of the lands on which the electronic component 4 was to be mounted.

Then, the electronic component 4 that was 1mm in length and 0.5mm in width was mounted onto the soldering paste 81 (82) (see FIG. 7b), and the assembly was processed in a reflow furnace to solder the electronic component 4 onto the component mounting board 1 (see FIG. 7c). Through the process described above, an electronic component device according to the present invention was obtained.

The electronic component device having undergone the soldering process is shown in FIG. 8. FIG. 8 is a cross-sectional view taken along line 8-8 in FIG. 7c. While the soldering paste in embodiment 3 contained the flux whose content was increased on purpose, the overall quantity of the solder that was actually used was reduced, and the electronic component 4 was soldered in a desirable state without becoming tilted, as shown in FIG. 8. In FIG. 7c, reference numeral 3 indicates the flux according to the present invention having been contained in the soldering paste 81 (82) that formed fillets on the outer sides of the terminal electrodes 41 and 42.

In addition, the use of the soldering paste containing the flux according to the present invention results in the periphery of the soldered electronic component 4 becoming covered by the flux improving the component shear strength. By increasing the flux content in the soldering paste on purpose in this manner, it becomes possible to control the solder thickness with the soldering paste. In particular, it was possible to prevent the electronic component 4 from being soldered in a misaligned state and to achieve a degree of bonding strength equal to or greater than that achieved in an electronic component in the prior art when the flux content was set to 35wt% or higher.

### Comparison example 3

For purposes of comparison, an electronic component 4 was mounted and soldered onto a component mounting board 1 using a soldering paste containing a rosin flux, as shown in FIG. 9, as in the prior art. To explain in more specific terms in reference to FIG. 9, the component mounting board 1 included two lands formed by sequentially laminating a Cu film 51 (52), an Ni film 61 (62) and an Au film 71 (72) (see FIG. 9a).

The soldering paste 91 (92) containing a rosin flux was applied onto each land at the component mounting board 1 (see FIG. 9a). The soldering paste 91 (92) was applied through a printing process implemented by using a metal mask having a 100 $\mu$ m thickness. The dimensions of the opening at the metal mask were set to 0.5mm X 0.3mm to match the dimensions of the lands on which the electronic component 4 was to be mounted.

Then, the electronic component 4 that was 1mm in length and 0.5mm in width was mounted onto the soldering paste 91 (92) (see FIG. 9b), and the assembly was processed in a reflow furnace to solder the electronic component 4 onto the component mounting board 1 (see FIG. 9c).

The electronic component device having undergone the reflow soldering process is shown in FIG. 10. FIG. 10 is a partial cross-sectional view taken along line 10-10 in FIG. 9c. As illustrated in FIG. 10, an excessive quantity of solder was used in the soldering process implemented with the soldering paste containing the rosin flux in the prior art, causing the electronic component 4 to be soldered in a misaligned



state. In FIG. 9c and FIG. 10, reference numeral 93 indicates the rosin flux having been contained in the soldering paste 91 (92).

#### Embodiment 4

Desirable compositions that could be adopted in the flux were examined. A liquid epoxy resin was used as the adhesive resin and abietic acid (carboxylic acid) was used as the hardening agent. The abietic acid was mixed at the weight ratios shown in TABLE I relative to the weight of the liquid epoxy resin. These flux samples were applied onto boards, the boards were then processed in a reflow furnace the temperature which was set to 230°C and the resulting hardened resin films were examined.

TABLE I

		mixing ratio (wt%)				
mixed substance	liquid epoxy resin	1	1	1	1	1
	abietic acid	0.25	0.5	0.75	1	1.5
	solvent	10	10	10	10	10
hardened film physical property		not hardened	gelled	hardened	hardened	gelled
hardened film hardness				elastic	hard	

As shown in TABLE I, when the mixing ratio of the abietic acid (wt%) to the liquid epoxy resin (wt%) was 1, a hardened film achieving a high degree of hardness was formed to achieve the best results. Any of the other mixing ratio settings in the table resulted in a failure to harden, a gel film or an elastic hardened film and is thus not appropriate. A desired hardened film (bondability) may be obtained at an arbitrary reflow

temperature by adjusting the mixing ratio of the epoxy resin or varying the quantity of the epoxy resin molecules and the number of functional groups and also by selecting the correct type of hardening agent (carboxylic acid) accordingly.

#### Embodiment 5

The flux prepared in embodiment 4 and solder powder were mixed to prepare a soldering paste. The soldering powder contained Sn (96.5) and Ag (3.5). The flux content was set at 25wt%. This soldering paste constitutes embodiment 5.

FIG. 11 shows the relationship between the reflow temperature and the component shear strength achieved by soldering a chip component with the soldering paste in embodiment 5 and the corresponding relationship achieved by soldering a chip component with a rosin soldering paste, used in the related art. In the figure, the curve L1 represents the characteristics achieved by using the soldering paste in embodiment 5 and the curve L2 represents the characteristics achieved by using the rosin soldering paste in the prior art.

As shown in FIG. 11, the soldering paste containing the flux demonstrates a higher degree of bonding strength compared to the rosin soldering paste in the prior art over a 220 to 260°C reflow temperature range. A particularly high degree of bonding strength is assured when the reflow temperature is set to 230°C or higher.

#### Embodiment 6

The soldering paste in embodiment 5 was used to solder a chip component onto a component mounting board, thereby obtaining an

electronic component device according to the present invention. An examination of the bondability between the component mounting board and terminal electrodes of the chip component conducted subsequently indicated that the soldering paste containing the flux achieves bondability similar to that of the rosin soldering paste. It is to be noted that when an evaluation similar to that made on the flux-containing soldering paste was performed on a conductive adhesive and an anisotropic conductive paste used in the prior art, it was confirmed that the component mounting board and the terminal of the component could not be bonded to each other.

While the electronic component 4 is mounted onto one surface of the component mounting board 1 in the embodiment described above, electronic components 4 may be mounted on the two surfaces of the component mounting board 1. In such a case, after executing a soldering process using the soldering paste according to the present invention at one surface of the component mounting board 1, an electronic component 4 may be soldered onto the other surface of the component mounting board 1 with a solder other than the soldering paste according to the present invention, e.g., a rosin soldering paste used in the prior art. Alternatively, the electronic components 4 may be soldered onto both surfaces of the component mounting board 1 with the soldering paste according to the present invention. In either case, problems such as the electronic component 4 becoming shifted, the Manhattan phenomenon (the raised component phenomenon) and the electronic component 4 falling off do not occur.

## 2. Application in electronic circuit modules

The electronic circuit module according to the present invention does not have any substantial difference from the electronic component device explained earlier except that the electronic component is replaced by a semiconductor chip and that the component mounting board is replaced by a chip mounting board in the electronic circuit module. In other words, the basic structure assumed in the electronic circuit module according to the present invention is essentially the structure disclosed with regard to the electronic component device. No specific restrictions are imposed with regard to the semiconductor chip used in the electronic circuit module. A semiconductor chip normally includes a semiconductor element (not shown) or a passive circuit element. It goes without saying that the present invention may be adopted in an electronic circuit module referred to as a chip-size package (CSP) as well.

FIG. 12 presents a partial cross-sectional view of the electronic circuit module according to the present invention. The electronic circuit module in the figure includes a semiconductor chip 100, a chip mounting board 200 and a soldering flux 400. At the semiconductor chip 100 in the figure, an appropriate number of terminal electrodes 110 and 120 are formed at appropriate positions at the lower surface or the like, and these terminal electrodes 110 and 120 are bonded to lands 230 and 240 on the chip mounting board 200 with solders 210 and 220 respectively.

The chip mounting board 200 may be constituted of a ceramic substrate, an organic resin substrate or through a combination thereof. Inside the chip mounting board 200, a single conductor pattern layer or a plurality of conductor pattern layers, via holes provided along the thicknesswise direction and the like are formed under normal circumstances. A conductor pattern may be provided to constitute a

capacitor, an inductor or the like as well as simply to facilitate circuit wiring.

The soldering flux 400 that contains an adhesive resin and a hardening agent is present between the semiconductor chip 100 and the chip mounting board 200 to bond them to each other. The soldering flux 400 is used as an adhesive.

In the embodiment shown in the figure, the soldering flux 400 fills the gap between the semiconductor chip 100 and the chip mounting board 200 almost completely.

As explained earlier, the soldering flux 400 does not need to be washed and can be directly utilized as an adhesive. As a result, an electronic circuit module can be manufactured at low production cost without having to implement a flux washing process. In addition, since the soldering flux that contains the adhesive resin and the hardening agent functions as an adhesive, a highly reliable electronic circuit module achieving a far greater durability in the solder joints is obtained.

FIG. 13 illustrates the soldering method adopted in an electronic circuit module such as the CSP (chip-size package) shown in FIG. 12. This soldering method is achieved by adopting the soldering method illustrated in FIG. 1 in conjunction with an electronic circuit module. As explained earlier, the flux 400 containing the adhesive resin and the hardening agent is applied onto the chip mounting board 200 at that the solder bumps 210 and 220 have been formed in advance. The solder bumps 210 and 220 are formed on lands 230 and 240 provided at the surface of the chip mounting board 200. The details of the soldering flux 400 have already been explained.

Then, the semiconductor chip 100 is mounted onto the chip mounting board 200. The semiconductor chip 100 is set on the chip mounting board 200 so as to position the terminal electrodes 110 and 120 on the solder bumps 210 and 220. Subsequently, the chip mounting board 200 having the semiconductor chip 100 mounted thereupon is processed in a reflow furnace to bond the terminal electrodes 110 and 120 provided at the two ends of a base body 40 of the semiconductor chip 100 to the solder bumps 210 and 220 through soldering. Through this process, the electronic circuit module shown in FIG. 12 is obtained.

FIG. 14 is a partial cross-sectional view of another example of the electronic circuit module according to the present invention. In the figure, the same reference numerals are assigned to parts identical to those shown in FIG. 12. In the embodiment presented in FIG. 14, the soldering flux 400 that is present between the semiconductor chip 100 and the chip mounting board 200 bonds them together around the solder bumps 210 and 220.

In this case, too, the soldering flux 400 does not need to be washed and can be directly utilized as an adhesive. As a result, an electronic circuit module can be manufactured at low production cost without having to implement a flux washing process. In addition, since the soldering flux that contains the adhesive resin and the hardening agent functions as an adhesive, a highly reliable electronic circuit module achieving a far greater durability in the solder joints is obtained.

FIG. 15 illustrates the soldering method adopted in the electronic circuit module shown in FIG. 14. This soldering method is achieved by adopting the soldering method shown in FIGS. 4, 5, 7 and 8 in conjunction with an electronic circuit module. The flux 410 and 420 containing the

soldering powder achieving the composition described earlier is applied onto the lands 230 and 240 formed in advance at the surface of the chip mounting board 200.

Then, the semiconductor chip 100 is mounted on the chip mounting board 200. The semiconductor chip 100 is set on the chip mounting board 200 so as to position the terminal electrodes 110 and 120 on the flux 410 and 420 containing the soldering powder. The chip mounting board 200 having the semiconductor chip 100 mounted thereupon is processed in a reflow furnace to bond the terminal electrodes 110 and 120 provided at the two ends of the semiconductor chip 100 through soldering by using the solder element included in the flux 410 and 420 containing the soldering powder.

In a bonded state, the adhesive resin and the hardening agent included in the flux 410 and 420 containing the soldering powder are present between the semiconductor chip 100 and the chip mounting board 200 to bond them together around the solder bumps 210 and 220. Through the process described above, the electronic circuit module shown in FIG. 14 is obtained. By increasing the quantities of the flux 410 and 420 containing the soldering powder used in the method illustrated in FIG. 15, the soldering flux 400 can be made to fill the gap between the semiconductor chip 100 and the chip mounting board 200 almost completely as shown in FIG. 12. Next, an explanation is given in reference to a specific embodiment and comparison example.

#### Embodiment 7

First, a soldering paste containing a thermo-setting flux achieving the following composition was prepared.

Flux: obtained by mixing bisphenol A resin and phthalic anhydride at a mass ratio of 1:1 and then adding a solvent at 10mass%

Soldering powder: Sn - 3.5 Ag

5 The flux was added into the soldering powder at a rate of 10mass%. The composition of the soldering powder can be selected in correspondence to the reflow temperature, and thus, another type of composition may be adopted. In addition, the quantity of the flux to be added, too, can be freely set.

10 The soldering paste containing the thermo-setting flux described above was applied onto an organic chip mounting board through screen printing. A screen with a metal mask thickness of 100 $\mu$ m was used in the screen printing process.

15 Next, a semiconductor chip having solder bumps formed thereupon in advance was mounted on the organic chip mounting board and the organic chip mounting board was then processed in a reflow furnace. The maximum reflow temperature during the furnace process was set to 240°C and the length of time over which the process was performed at 220°C or higher was set to 30 seconds.

#### Comparison example 7

20 For purposes of comparison, products of the known art were prepared by using a sealant and also without using a sealant.

(Tests)

25 The electronic circuit module achieved in embodiment 7 and the two types of electronic circuit modules in the related art underwent thermal shock tests. The thermal shock tests were each conducted over



2000 cycles, with the temperature sustained at -55 °C over 0.5 hours and then sustained at + 125°C over 0.5 hours during each cycle. Then, the DC resistances (RDCs) at the soldered connector portions were measured.

FIG. 16 presents a graph of the results of the RDC measurement. In FIG. 16, the horizontal axis represents the number of thermal shock cycles and the vertical axis represents the RDC. The characteristics L11 were achieved by the prior art product prepared without using a sealant, the characteristics L12 were achieved by the prior art product prepared by using a sealant and the characteristics L13 were achieved by embodiment 7 of the present invention.

As the characteristics L11 in FIG. 16 indicate, the RDC increased drastically in the prior art product prepared without using a sealant once the number of cycles in the thermal shock test exceeded 1500, manifesting a deterioration in the RDC. No such deterioration in the RDC was observed in embodiment 7 according to the present invention even after the number of cycles exceeded 2000, as indicated by the characteristics L13. These characteristics L13 are equivalent to the characteristics L12 achieved in the prior art product by using a sealant.

### 3. Application in electronic circuit apparatuses

The electronic circuit apparatus according to the present invention does not have any substantial difference from the electronic component device explained earlier except that the electronic component is replaced by an electronic circuit module and that the component mounting board is replaced by a motherboard in the electronic circuit apparatus. In other words, the basic structure assumed in the electronic circuit apparatus

according to the present invention is essentially the structure disclosed in reference to the electronic component device.

FIG. 17 presents a partial cross-sectional view of the electronic circuit apparatus according to the present invention. The electronic circuit apparatus in the figure includes an electronic circuit module 300, a motherboard 500 and a soldering flux 600.

While the electronic circuit module 300 may be an electronic circuit module of the existing art, it is desirable to utilize an electronic circuit module assuming the structure illustrated in FIG. 12 or 14. At the electronic circuit module 300, an appropriate number of terminal electrodes 250 and 260 are formed at appropriate positions at the lower surface thereof for instance, and these terminal electrodes 250 and 260 are bonded to lands 530 and 540 on the motherboard 500 with solder bumps 510 and 520 respectively.

The motherboard 500 may be constituted of a ceramic substrate, an organic resin substrate or through a combination thereof. Inside the motherboard 500, a single conductor pattern layer or a plurality of conductor pattern layers, via holes provided along the thicknesswise direction and the like may be formed. A conductor pattern is provided to constitute a capacitor, an inductor or the like or simply to facilitate circuit wiring.

The soldering flux 600 that contains an adhesive resin and a hardening agent, is present between the electronic circuit module 300 and the motherboard 500 to bond them to each other. The soldering flux 600 is used as an adhesive. In the embodiment shown in the figure, the soldering flux 600 fills the gap between the electronic circuit module 300 and the motherboard 500 almost completely.

The soldering flux 600 does not need to be washed and can be directly utilized as an adhesive. As a result, an electronic circuit apparatus can be manufactured at low production cost without having to implement a flux washing process. In addition, since the soldering flux that contains the adhesive resin and the hardening agent functions as an adhesive, a highly reliable electronic circuit apparatus achieving a far greater durability in the solder joints is obtained.

FIG. 18 illustrates the soldering method adopted in the electronic circuit apparatus shown in FIG. 17. This soldering method is achieved by adopting the soldering method illustrated in FIG. 1 in conjunction with an electronic circuit apparatus. As explained earlier, the flux 600 containing the adhesive resin and the hardening agent is applied onto the motherboard 500 at which the solder bumps 510 and 520 have been formed in advance. The flux 600 does not contain any soldering powder.

The solder bumps 510 and 520 are formed on the lands 530 and 540 provided at a surface of the motherboard 500. Then, the electronic circuit module 300 is mounted onto the motherboard 500. The electronic circuit module 300 is set on the motherboard 500 so as to position the terminal electrodes 250 and 260 on the solder bumps 510 and 520. Subsequently, the motherboard 500 having the electronic circuit module 300 mounted thereupon is processed in a reflow furnace to bond the terminal electrodes 250 and 260 provided at the electronic circuit module 300 to the solder bumps 510 and 520 through soldering. Through this process, the electronic circuit apparatus shown in FIG. 17 is obtained.

FIG. 19 is a partial cross-sectional view of another example of the electronic circuit apparatus according to the present invention. In the figure, the same reference numerals are assigned to parts identical to those

shown in FIG. 17. In the embodiment presented in FIG. 19, the soldering flux 600 that is present between the electronic circuit module 300 and the motherboard 500 bonds them together around the solder bumps 510 and 520.

5 In this case, too, the soldering flux 600 does not need to be washed and can be directly utilized as an adhesive. As a result, an electronic circuit apparatus can be manufactured at low production cost without having to implement a flux washing process. In addition, since the soldering flux 600 that contains the adhesive resin and the hardening agent  
10 in addition to the soldering powder, functions as an adhesive, a highly reliable electronic circuit apparatus achieving a far greater durability in the solder joints is obtained.

FIG. 20 illustrates the soldering method adopted in the electronic circuit apparatus shown in FIG. 19. This soldering method is achieved by  
15 adopting the soldering method shown in FIGS. 4 through 8 in conjunction with an electronic circuit apparatus. The flux 610 and 620 containing the soldering powder achieving the composition described earlier is applied onto the lands 530 and 540 formed in advance at the surface of the motherboard 500. Then, the electronic circuit module 300 is mounted on  
20 the motherboard 500. The electronic circuit module 300 is set on the motherboard 500 so as to position the terminal electrodes 250 and 260 on the flux 610 and 620 containing the soldering powder.

Next, the motherboard 500 having the electronic circuit module 300 mounted thereupon is processed in a reflow furnace to bond the  
25 terminal electrodes 250 and 260 provided at the electronic circuit module 300 through soldering by using the solder element included in the flux 610 and 620 containing the soldering powder. The adhesive resin and the

hardening agent included in the flux 610 and 620 containing the soldering powder are present between the electronic circuit module 300 and the motherboard 500 to bond them together around the solder bumps 510 and 520. Through the process described above, the electronic circuit apparatus shown in FIG. 19 is obtained. By increasing the quantities of the flux 610 and 620 containing the soldering powder used in the method illustrated in FIG. 20, the soldering flux 600 can be made to fill the gap between the electronic circuit module 300 and the motherboard 500 almost completely as shown in FIG. 17. Next, an explanation is given in reference to specific embodiments and comparison examples.

#### Embodiment 8

First, a soldering paste containing a thermo-setting flux achieving the following composition was prepared.

Flux: obtained by mixing bisphenol A resin and phthalic anhydride at a mass ratio of 1:1 and then adding a solvent at 10mass%

Soldering powder: Sn - 3.5 Ag

The flux was added into the soldering powder at a rate of 10mass%. The composition of the soldering powder can be selected in correspondence to the reflow temperature, and thus, another type of composition may be adopted. In addition, the quantity of the flux to be added, too, can be freely set.

The motherboard was constituted by using an organic motherboard. The electronic circuit module was constituted by mounting a semiconductor chip on an organic chip mounting board. The surfaces of the terminal electrodes provided at the lower surface of the organic chip

mounting board were processed through an Au plating process so as to allow them to be soldered to the organic motherboard.

5 The soldering paste containing the thermo-setting flux described above was applied onto the organic chip mounting board through screen printing. A screen with a metal mask thickness of 100 $\mu$ m was used in the screen printing process.

10 Next, the electronic circuit module was mounted on the organic motherboard and the organic motherboard was then processed in a reflow furnace. The maximum reflow temperature during the furnace process was set to 240°C and the length of time over which the process was performed at 220°C or higher was set to 30 seconds.

#### Embodiment 9

15 An electronic circuit apparatus was manufactured by using a ceramic substrate to constitute the chip mounting board of the electronic circuit module, by following the procedure taken in embodiment 8.

#### Comparison example 8

20 An electronic circuit module having a chip mounting board constituted of an organic material was soldered to an organic motherboard with a flux of the related art. The surfaces of the terminal electrodes provided at the lower surface of the organic chip mounting board were processed through an Au plating process so as to allow them to be soldered to the organic motherboard.

#### Comparison example 9

An electronic circuit module having a chip mounting board constituted of a ceramic material was soldered to an organic motherboard with a flux of the related art. The surfaces of the terminal electrodes provided at the lower surface of the ceramic chip mounting board were processed through an Au plating process so as to allow them to be soldered to the organic motherboard.

(Tests)

The electronic circuit modules achieved in embodiments 8 and 9 and the two types of electronic circuit modules in the related art prepared in comparison examples 8 and 9 underwent thermal shock tests. The thermal shock tests were conducted over 2000 cycles, with the temperature sustained at -55 °C over 0.5 hours and then sustained at +125°C over 0.5 hours during each cycle. Then, the DC resistances (RDCs) at the soldered connector portions were measured.

FIG. 21 presents a graph of the results of the RDC measurement. In FIG. 21, the horizontal axis represents the number of thermal shock cycles and the vertical axis represents the RDC. The characteristics L21 were achieved in comparison example 9 and the characteristic L22 were achieved in embodiments 8 and 9 and comparison example 8.

In FIG. 21, the RDC measured in comparison example 9 with a ceramic material constituting the chip mounting board of the electronic circuit module and an organic material constituting the motherboard increased drastically, manifesting deterioration in the RDC once the number of test cycles exceeded 1000 as indicated by the characteristics L21. In contrast, embodiments 8 and 9 of the present invention did not manifest any deterioration in the RDC even when the number of test

cycles exceeded 2000 cycles as indicated by the characteristics L22. It is a point of special interest to be noted that while a pronounced RDC deterioration results when an electronic circuit module in the prior art having a chip mounting board constituted of a ceramic substrate and a motherboard constituted of an organic resin substrate (see the characteristics L21) are used in combination, it is possible to prevent such an RDC deterioration even in the same substrate combination by adopting the present invention (see the characteristics L22) as the comparison of the characteristics L22 representing embodiment 9 and the characteristics L21 representing comparison example 9 clearly indicates.

As described earlier, an electronic component is bonded onto a component mounting board with the adhesive resin contained in the flux according to the present invention. Then, when the electronic component has been bonded to the component mounting board, the assembly undergoes an inspection process during which the soldering state is inspected. If the solder is defective, it is desirable to disengage the electronic component from the component mounting board to re-solder the electronic component onto the component mounting board. If the electronic component cannot be separated from the component mounting board, the electronic component and the component mounting board must be discarded, that leads to an increase in production cost and a reduction in yield. Accordingly, it is desirable to constitute the flux and the soldering paste according to the present invention so as to allow the soldered electronic component to be separated from the component mounting board.

In addition, the use of Pb-free solders has become common in recent years as part of the effort made to protect the global environment



and solder elements achieving various compositions are used in such Pb-free solders. Since solder elements with different compositions have varying melting points, the reflow temperature must be adjusted in conformance to the composition of the solder element in use. For this reason, it is necessary to develop a technology that makes it possible to set the adhesive resin in correspondence to a given reflow temperature setting.

While an explanation is given above on an example in that an electronic component is soldered onto a component mounting board, the same concept applies when soldering a semiconductor chip to a chip mounting board and when soldering an electronic circuit module onto a motherboard.

Next, the compositions of the flux and the soldering paste that address the concerns discussed above are disclosed. Namely, the hardening agent in the flux should contain at least two types of carboxylic acids having melting points different from each other. The carboxylic acid with the lower melting point functions as a hardening agent at the reflow temperature. The carboxylic acid with the higher melting point, on the other hand, lowers the viscosity of the adhesive resin at a repair temperature set higher than the reflow temperature.

Since the viscosity of the adhesive resin is lowered by raising the temperature of the flux to a level approximately matching the repair temperature to cause the carboxylic acid with the higher melting point to melt, the electronic component can be separated from the component mounting board. As a result, it is possible to re-solder the electronic component as in the prior art in which a rosin flux is used, while retaining the bonding function. An adhesive flux adopting such a composition is

particularly beneficial when soldering large, expensive packages such as electronic circuit modules.

In addition, when carboxylic acids with different melting points are mixed, it is possible to vary the thermo-setting temperature of the adhesive resin. This allows the adhesive resin to be hardened at a given reflow temperature, thereby making it possible to adjust the reflow temperature in conformance to the melting points of the solder elements that are used.

Furthermore, by combining various carboxylic acids or changing the mixing ratio thereof, too, the thermo-setting temperature of the adhesive resin can be adjusted, to achieve similar effects and advantages.

Next, the soldering paste should contain the flux assuming the composition described above and soldering powder. As a result, the soldering paste, too, achieves effects and advantages similar to those realized in the flux assuming the composition described above. The effects and advantages are now explained in more specific terms in reference to test data.

Test 1: repairability achieved by combining a plurality of carboxylic acids

First, test 1 was conducted to obtain a flux and a soldering paste achieving outstanding repairability. In tests 1, bisphenol A was used as the thermo-setting resin. The hardening agent was constituted by using two different carboxylic acids, and more specifically, by using adipic acid and pimelic acid. Both adipic acid and pimelic acid belong to the carboxylic acid group. Adipic acid and pimelic acid have different melting points. More specifically, the melting point of adipic acid is 153°C, whereas the melting point of pimelic acid is 279°C. Bisphenol A, adipic acid and

pimelic acid were mixed at the mixing ratios (mass%) listed in TABLE II below to prepare flux samples 1 through 4. The ratio of the adipic acid content a (mass%) and the pimelic acid content b (mass%) is varied within a range expressed as  $a : b = (50 : 50)$  to  $(95 : 5)$  when  $a + b = 100$  among flux samples 1 through 4. Bisphenol A and adipic acid were mixed at the mixing ratio (mass%) in TABLE II below to prepare flux sample 5.

TABLE II

		sample 1	sample 2	sample 3	sample 4	sample 5
mixing ratio (mass%)	bisphenol A	1	1	1	1	1
	adipic acid	0.5	0.75	0.85	0.95	1
	pimelic acid	0.5	0.25	0.15	0.05	0

Next, soldering pastes were prepared by mixing soldering powder into flux samples 1 through 5 prepared as described above. The soldering powder was constituted of Sn-3.5Ag. The composition of the soldering powder can be selected in correspondence to the reflow temperature, and soldering powder assuming a composition other than that used in this example may be adopted. The quantity of the flux that was mixed relative to the quantity of the soldering powder to constitute a soldering paste was set at 25mass%. However, the quantity of the flux that is mixed in, too, can be set freely.

An electronic circuit module was soldered onto a motherboard using each of the soldering pastes assuming the compositions described above. FIGS. 22 through 25 present partial cross-sectional views illustrating the process through which the electronic circuit module was

soldered onto the motherboard. In the figures, the same reference numerals are assigned to portions identical to those shown in FIG. 19.

As shown in FIG. 22, lands 530 and 540 were provided at the motherboard 500. The motherboard 500 may be constituted of a ceramic substrate, an organic resin substrate or a material achieved through a combination thereof. The motherboard 500 in the figure is constituted of an organic resin substrate.

As shown in FIG. 23, a soldering paste 610 and 620 achieved in embodiment 10 was applied onto the lands 530 and 540 at the motherboard 500. The soldering paste 610 and 620 was applied through a printing process implemented by using a metal mask.

Then, as shown in FIG. 24, an electronic circuit module 300 was mounted onto the motherboard 500. The electronic circuit module 300 in the figure assumed a structure similar to that of the electronic circuit module shown in FIG. 12. The electronic circuit module 300 was a CSP (chip-size package) electronic circuit module that is referred to as an LGA-structured product. The chip mounting board 200 of the electronic circuit module 300 is a ceramic substrate. As illustrated in FIG. 24, the electronic circuit module 300 was set on the motherboard 500 so as to position terminal electrodes 250 and 260 onto the soldering paste 610 and 620.

Next, the motherboard 500 mounted with the electronic circuit module 300 was processed in a reflow furnace to bond the terminal electrodes 250 and 260 provided at the electronic circuit module 300 with the soldering elements contained in the soldering paste 610 and 620. The adhesive resin and the hardening agent included in the soldering paste 610 and 620 were present between the electronic circuit module 300 and the

motherboard 500 to bond them together around the solder bumps 510 and 520. As a result, an electronic circuit apparatus according to the present invention was obtained as shown in FIG. 25. In FIG. 25, reference numeral 600 indicates the flux achieved in embodiment 10 that was contained in the soldering paste 610 (620), and subsequently formed fillets on the outer sides of the terminal electrodes 250 and 260.

As explained earlier, the flux 600 contained the adhesive resin and the hardening agent. The hardening agent contained adipic acid and pimelic acid having melting points different from each other. Adipic acid with the lower melting point functioned as a hardening agent at the reflow temperature. Pimelic acid with the higher melting point lowered the viscosity of the adhesive resin at the repair temperature set higher than the reflow temperature.

The following is a detailed explanation of the individual flux samples 1 through 5. When flux sample 5 was used, adipic acid contained in the flux became melted, causing the solder to melt during the reflow furnace process. As a result, the lands formed at the motherboard and the terminal electrodes at the electronic circuit module became bonded to each other.

When flux samples 1 and 2 were used, the solder did not melt during the reflow furnace process due to the pimelic acid hardening process contained in the fluxes that continued in the reflow furnace. As a result, the lands at the motherboard and the terminal electrodes at the electronic circuit module failed to bond to each other.

When flux samples 3 and 4 were used, adipic acid contained in the flux became melted, causing the solder to melt during the reflow furnace process. As a result, the lands 530 and 540 formed at the

motherboard 500 and the terminal electrodes 250 and 260 at the electronic circuit module 300 became bonded to each other.

FIGS. 26 and 27 present partial cross-sectional views illustrating the process through which the electronic circuit module is separated from comparedthe motherboard. As shown in FIG. 26, hot air is blown onto the flux 600 to raise the temperature of the flux 600 to approximately match the repair temperature, thereby causing the pimelic acid with the higher melting point to become melted and thus lowering the viscosity of the adhesive resin. Consequently, the electronic circuit module 300 can be separated from the motherboard 500 as illustrated in FIG. 27.

As a result, it becomes possible to re-solder the electronic circuit module 300 as in the prior art in that a rosin flux is used, while sustaining the adhesive function. The adhesive flux assuming such a composition is particularly advantageous in application in that an expensive electronic circuit module 300 such as a large package is soldered. In addition, the repair process can be executed using the adhesive flux at a similar level of ease of work compared to that in the prior art.

The repair process is now explained in detail. Repair tests were conducted on flux samples 3 through 5 that enabled reflow mounting among flux samples 1 through 5. In the repair tests, hot air achieving a temperature of approximately 300°C was blown into the area where the chip mounting board 200 of the electronic circuit module 300 and the motherboard 500 were bonded to each other.

Flux sample 5 bonding the chip mounting board of the electronic circuit module and the motherboard to each other first become softened but was caused to hardened again by the hot air. As a result, the electronic circuit module could not be peeled off the motherboard.

When hot air was blown into the area where the chip mounting board 200 of the electronic circuit module 300 and the motherboard 500 were bonded to each other by using the flux 600 in sample 3 or 4, pimelic acid contained in the flux 600 became melted again, and thus, it was possible to peel off the electronic circuit module 300 from the motherboard 500.

This leads to the conclusion that in order to enable repair work to soldered electronic circuit module 300, pimelic acid having a melting point that is close to the repair temperature should be mixed in addition to adipic acid, that is used to harden the adhesive resin. When the pimelic acid melts at the repair temperature, the viscosity of the adhesive resin is reduced. Consequently, the electronic circuit module 300 having been soldered onto the motherboard 500 can be separated from the motherboard 500 for repair.

The results of test 1 described above indicate that it is desirable to add pimelic acid for repair purposes at 15 mass% to 5 mass% relative to the content of adipic acid to be used to harden the adhesive resin. Namely, the adipic acid content a (mass%) and the pimelic acid content b (mass%) should satisfy  $a : b = (85:15)$  to  $(95:5)$  when  $a + b = 100$ .

The soldering process was implemented using the soldering pastes obtained by mixing the fluxes prepared in test 1 with soldering powder. However, similar effects and advantages may be achieved in a soldering process implemented by applying the fluxes prepared in test 1 onto a substrate having solder bumps formed thereupon instead.

The electronic component device according to the present invention does not have any substantial difference from the electronic circuit apparatus described earlier except that the electronic circuit module

is replaced by an electronic component and the motherboard is replaced by a component mounting board in the electronic component device. In other words, the basic structure assumed in the electronic component device according to the present invention is essentially the same as that disclosed in reference to the electronic circuit apparatus.

Likewise, the electronic circuit module according to the present invention does not have any substantial difference from the electronic circuit apparatus described earlier except that the electronic circuit module is replaced by a semiconductor chip and the motherboard is replaced by a chip mounting board in the electronic circuit module. In other words, the basic structure assumed in the electronic circuit module according to the present invention is essentially the same as that disclosed in reference to the electronic circuit apparatus. Next, the following tests 2 through 5 were conducted to achieve variance in the reflow temperature.

Test 2: thermo-setting characteristics of fluxes containing a single carboxylic acid.

First, the thermo-setting characteristics of fluxes each prepared by mixing in a single carboxylic acid as a hardening agent were examined through test 2. The thermo-setting resin was constituted of bisphenol A. The hardening agent in each of the fluxes was constituted of one of adipic acid, maleic acid, succinic acid and pimelic acid, as shown in TABLE III below. Each flux was prepared by mixing the thermo-setting resin and the hardening agent at a mass ratio of 1:1. 2g of each of the fluxes thus prepared was applied onto a Cu plate, the Cu plate was then processed in a reflow furnace at 240°C and the behaviors of the films formed on the Cu plates were compared with one another.



TABLE III

	sample 11	sample 12	sample 13	sample 14
thermo-setting resin	bisphenol A	bisphenol A	bisphenol A	bisphenol A
hardening agent	adipic acid	maleic acid	succinic acid	pimelic acid
melting point of hardening agent	153°C	133°C	188°C	279°C
film behaviour	gelled	hardened at room temperature	hardened	hardened

As shown in TABLE III, flux samples 11 and 12 that manifested gelling of the film or hardening of the film at room temperature, are not desirable. In contrast, flux samples 13 and 14, that allowed the films to harden in the reflow furnace process, achieved desirable results.

Through a comparison of the extents to which the process of hardening progressed in flux samples 11 through 14, it was confirmed that the process of thermo-setting is further promoted as the melting point of the hardening agent becomes higher with the hardening process of the flux-containing adipic acid as the hardening agent (sample 11) slowed down to the greatest degree. If adipic acid and succinic acid are mixed together to constitute a hardening agent, a harder film is obtained by increasing the mixing ratio of the succinic acid.

Test 3: variance in reflow temperature achieved by mixing a plurality of carboxylic acids

Based upon the results of test 2 described above, the variance in the reflow temperature achieved by mixing adipic acid and succinic acid was examined. Bisphenol A was used as the thermo-setting resin. The hardening agent was constituted by using two carboxylic acids, i.e. adipic acid and succinic acid. Both adipic acid and succinic acid belong to the

carboxylic acid group. The melting points of adipic acid and succinic acid are different from each other. More specifically, the melting point of adipic acid is 153°C, whereas the melting point of succinic acid is 188°C. Bisphenol A, adipic acid and succinic acid were mixed at a mass ratio of 1: 0.5 : 0.5 to prepare a flux.

Next, the prepared flux was mixed with soldering powder to prepare a soldering paste. The soldering powder was constituted of Sn-3.5Ag, and the quantity of the flux that was mixed relative to the quantity of the soldering powder was 15 mass%. Hereafter, this soldering paste is referred to as soldering paste sample 22. The composition of the soldering powder may be selected in conformance to the reflow temperature, and thus, the soldering powder may assume a composition other than that described above. The quantity of the flux that is mixed, too, can be freely set.

In addition, bisphenol A and adipic acid were mixed at a mass ratio of 1:1 to prepare a flux. Then, the flux thus prepared was mixed with soldering powder to prepare a soldering paste. As in the case of soldering paste sample 22, the soldering powder was constituted of Sn-3.5Ag and the quantity of the flux that was mixed relative to the quantity of the soldering powder was set at 15 mass%. Hereafter, this soldering paste is referred to as soldering paste sample 21.

FIG. 28 shows relationships between the reflow temperature and the component shear strength observed when capacitors were soldered using soldering paste samples 21 and 22. Two different types of test capacitors, i.e., 1005-size STD terminal capacitors and 1005-size CSB terminal capacitors, were used in the test. An STD terminal capacitor includes terminals formed at the two side surfaces of the capacitor element

assembly. A CSB terminal capacitor includes terminals formed at the two ends of the bottom surface of the capacitor element assembly.

In FIG. 28, the characteristics L31 were achieved by using soldering paste sample 21 to solder STD terminal capacitors, characteristics L32 were achieved by using soldering paste sample 22 to solder STD terminal capacitors, the characteristics L41 were achieved by using soldering paste sample 21 to solder CSB terminal capacitors and characteristics L42 were achieved by using soldering paste sample 22 to solder CSB terminal capacitors. The component shear strength was measured through the method illustrated in FIG. 2.

As shown in FIG. 28, when soldering STD terminal capacitors, a degree of component shear strength equivalent to that realized with soldering paste sample 21 (see the characteristics L31) was achieved by using soldering paste sample 22 (see the characteristics L32) at a lower reflow temperature.

In addition, when soldering CSB terminal capacitors, a degree of component shear strength equivalent to that realized with soldering paste sample 21 (see the characteristics L41) was achieved by using soldering paste sample 22 (see the characteristics L42) at a lower reflow temperature. For instance, assuming that 0.9kg of component shear strength is required, the reflow temperature must be set to 224 to 230 °C in conjunction with soldering paste sample 21 (see the characteristics L41), whereas the reflow temperature only needs to be set to 217 to 221°C when soldering paste sample 22 (see the characteristics L42) is used to achieve the required component shear strength.

As explained above, by mixing carboxylic acids (adipic acid and succinic acid) with melting points different from each other, the variance

in thermo-setting temperature of the adhesive resin (bisphenol A) can be achieved. As a result, it becomes possible to harden the adhesive resin (bisphenol A) at a desired reflow temperature, that means that the reflow temperature can be adjusted in correspondence to the melting point of the solder element that is used. Thus, the reflow temperature does not need to be fixed to a specific value.

In addition, the thermo-setting temperature of the adhesive resin (bisphenol A) can also be varied by using a different combination of carboxylic acids in the mixture (adipic acid and succinic acid) to achieve similar effects and advantages. For instance, by switching to a combination of adipic acid and pimelic acid from the combination of adipic acid and succinic acid, the thermo-setting temperature of the adhesive resin (bisphenol A) is changed.

#### Test 4: variance in reflow temperature achieved through adjustment of carboxylic acid mixing ratio

Next, the variance in the reflow temperature achieved by adjusting the mixing ratio of carboxylic acids (adipic acid and succinic acid) in the mixture was examined. Bisphenol A was used as the thermo-setting resin. The hardening agent was constituted by using adipic acid and succinic acid. bisphenol A, adipic acid and succinic acid were mixed at mixing ratios (mass%) in TABLE IV below to prepare fluxes. The adipic acid content  $a$  (mass%) and the succinic acid content  $c$  (mass%) were varied within the range of  $a : c = (100 : 0)$  to  $(0 : 100)$  when  $a + c = 100$  among these fluxes.

Soldering powder was mixed with the individual fluxes prepared, and soldering paste samples 50 through 55 were prepared. The soldering

powder was constituted of eutectic solder (Sn63 - Pb37) powder. The quantity of the flux that was mixed relative to the quantity of the soldering powder in each sample was set at 15 mass%. The composition of the soldering powder may be selected in correspondence to the reflow temperature setting, and thus, soldering powder with a different composition may be used. The quantity of the flux that is added, too, may be set freely as well.

TABLE IV

		sample 50	sample 51	sample 52	sample 53	sample 54	sample 55
mixing ratio ratio (mass%)	bisphenol A	1	1	1	1	1	1
	adipic acid	1	0.95	0.75	0.5	0.25	0
	succinic acid	0	0.05	0.25	0.5	0.75	1
soldering powder		eutectic	eutectic	eutectic	eutectic	eutectic	eutectic
soldering results		OK	OK	OK	OK	OK	OK

As shown in TABLE IV, terminals were connected in a desirable manner through soldering by using soldering paste sample 50. However, soldering paste sample 50, that only contains adipic acid as the hardening agent, does not allow the thermo-setting temperature of the adhesive resin (bisphenol A) to be varied and thus, does not allow the reflow temperature to be varied either.

In addition, the flux in soldering paste sample 55 became hardened before the solder melted, and, as a result, the terminals could not be bonded with soldering paste sample 55.

In contrast, terminals were connected in a desirable manner through soldering by using soldering paste samples 51 through 54.

FIG. 29 shows relationships between the reflow temperature and the component shear strength observed when capacitors were soldered with soldering paste samples 50 through 54. 1005-size STD terminal capacitors were used in the test. In FIG. 29, the characteristics L50 to L54 respectively represent the characteristics achieved by using soldering paste samples 50 through 54.

The required component shear strength can be assured even at a low reflow temperature by increasing the succinic acid content. For instance, assuming that 1.4 kg of component shear strength is required, the reflow temperature needs to be set at 230°C in conjunction with soldering paste sample 52 (see the characteristics L52). In contrast, soldering paste sample 53 (see the characteristics L53) with a higher succinic acid content than sample 52 only requires the reflow temperature to be set to 210°C.

As explained above, by changing the mixing ratio of the carboxylic acids (adipic acid and succinic acid) in the mixture, the thermo-setting temperature of the adhesive resin (bisphenol A) can be adjusted. As a result, it becomes possible to harden the adhesive resin (bisphenol A) at any reflow temperature, that means that the reflow temperature can be adjusted in correspondence to the melting point of the solder element that is used. Thus, the reflow temperature does not need to be fixed to a specific value.

The results of test 4 described above indicate that it is desirable that the adipic acid content  $a$  (mass%) and the succinic acid content  $c$  (mass%) satisfy  $a : c = (95 : 5)$  to  $(25 : 75)$  with  $a + c = 100$ , when adipic acid and succinic acid are mixed to constitute the hardening agent to be used in conjunction with a eutectic solder (Sn63-Pb37) constituting the solder element.

# Test 5: varying solder element to be combined with flux

The solder element used in test 4 was constituted of a eutectic solder (Sn63-Pb37). The melting point of the eutectic solder (Sn63-Pb37) is 183°C. Now, a test (hereafter referred to as test 5) conducted by using Sn-3.5Ag solder to constitute the solder element unlike in test 4 is explained. The melting point of the Sn-3.5Ag solder is 217°C, that is considerably higher than the melting point, i.e. 183°C, of the eutectic solder (Sn63-Pb37).

Bisphenol A, adipic acid and succinic acid were mixed at a mixing ratio (mass%) in TABLE V to prepare fluxes. The adipic acid content a (mass%) and the succinic acid content c (mass%) were varied within the range of a : c = 100 : 0 to 0 : 100 (a + c = 100) among these fluxes.

Soldering powder was mixed with the individual fluxes prepared, and soldering paste samples 70 through 75 were prepared. The soldering powder was constituted of Sn-3.5Ag soldering powder. The quantity of the flux that was mixed relative to the quantity of the soldering powder in each sample was set at 15 mass%.

TABLE V

		sample 70	sample 71	sample 72	sample 73	sample 74	sample 75
mixing ratio (mass%)	bisphenol A	1	1	1	1	1	1
	adipic acid	1	0.95	0.75	0.5	0.25	0
	succinic acid	0	0.05	0.25	0.5	0.75	1
soldering powder		Sn-3.5Ag	Sn-3.5Ag	Sn-3.5Ag	Sn-3.5Ag	Sn-3.5Ag	Sn-3.5Ag
soldering results		OK	OK	NG	NG	NG	NG

As shown in TABLE V, the fluxes in soldering paste samples 72 through 75 became hardened before the solder melted, and, as a result, the terminals could not be bonded with soldering paste samples 72 through 75.

5 In addition, terminals were connected in a desirable manner through soldering by using soldering paste samples 70 and 71.

The results of test 5 described above indicate that it is desirable that the adipic acid content a (mass%) and the succinic acid content c (mass%) satisfy  $a : c = (100 : 0)$  to  $(95 : 5)$  with  $a + c = 100$ , when adipic  
10 acid and succinic acid are mixed to constitute the hardening agent to be used in conjunction with Sn-3.5Ag solder constituting the solder element.

In tests 3 through 5 described above, the soldering process was implemented by using soldering pastes obtained by mixing fluxes with soldering powder. However, similar effects and advantages may be  
15 achieved by implementing a soldering process with a flux directly applied onto a board having solder bumps formed thereupon.

It is obvious that an electronic circuit apparatus similar to the electronic circuit apparatus shown in FIG. 25 may be achieved by using any of the solderings pastes checked in tests 3 through 5. In addition, an  
20 electronic component device and an electronic circuit module can be also constituted by using any of the soldering tastes checked in tests 3 through 5.

Furthermore, by mixing carboxylic acids with different thermo-setting speeds from each other in the hardening agent, the speed  
25 with which the adhesive resin becomes thermally set can be varied. As a result, it becomes possible to adjust the reflow time or the reflow temperature in correspondence to the solder element that is used. For



instance, when adipic acid and succinic acid are used in combination, the quantity of heat required to thermally set succinic acid is smaller than the quantity of heat required to thermally set adipic acid, and thus, the thermo-setting speed of succinic acid is higher than the thermo-setting speed of adipic acid. For this reason, it is possible to vary the thermo-setting speed of the adhesive resin (bisphenol A) by mixing adipic acid and succinic acid to constitutes the hardening agent, in order to adjust the reflow time or the reflow temperature. A similar advantage may be achieved through a different combination of carboxylic acids such as adipic acid and pimelic acid.

Moreover, the thermo-setting speed of the adhesive resin can also be varied by combining different carboxylic acids in the mixture or by changing the mixing ratio, to achieve similar effects and advantages.

Although not shown, in conjunction with the soldering paste according to the present invention, the flux according to the present invention may be used as a sealant to bond an electronic component to a component mounting board, a semiconductor chip to a chip mounting board or an electronic circuit module to a motherboard with the sealant. Alternatively, a standard sealant may be formed at the upper layer of the flux according to the present invention applied onto a component mounting board.

## INDUSTRIAL APPLICABILITY

As explained above, the present invention achieves the following advantages.

(a) A soldering flux, a soldering paste and a soldering method that

achieve a sufficient degree of bonding strength even when components are mounted at a higher density, components are further miniaturized or components are set with a smaller pitch, are provided.

5 (b) A soldering flux, a soldering paste and a soldering method, that make it possible to prevent components from becoming lifted off or falling off from a double-sided installation type component mounting board with a high degree of reliability, are provided.

10 (c) An electronic component device, an electronic circuit module and an electronic circuit apparatus, that do not require a flux quashing process and thus can be manufactured at lower production cost are provided.

15 (d) An electronic component device, an electronic circuit module and an electronic circuit apparatus achieving a high degree of reliability by greatly increasing the durability of solder joints are provided.

What is claimed is:

1. A soldering flux-containing an adhesive resin and a hardening agent.
2. The flux of claim 1, assuming either a liquid form or a paste form.
3. The flux of either claim 1 or claim 2 wherein:  
said adhesive resin contains a thermo-setting resin.
4. The flux of claim 3, wherein:  
said thermo-setting resin is constituted of at least one of an epoxy resin, a phenol resin, a polyimide resin, a silicon resin, a modified resin and an acrylic resin.
5. The flux of any of claims 1 through 4, wherein:  
said hardening agent contains a carboxylic acid.
6. The flux of any of claims 1 through 4, wherein:  
said hardening agent contains at least two different carboxylic acids with said two carboxylic acids having melting points different from each other.
7. The flux of claim 6, wherein:  
one of said two carboxylic acids is adipic acid and the other carboxylic acid is pimelic acid.
8. The flux of claim 7, wherein:  
 $a : b = (85 : 15) \text{ to } (95 : 5)$  is satisfied with "a" representing the content (mass%) of the adipic acid and "b" representing the content (mass%) of the pimelic acid.
9. The flux of claim 6, wherein:  
one of said two carboxylic acids is adipic acid and the other carboxylic acid is succinic acid.

10. The flux of claim 9, wherein:

a : c = (95 : 5) to (25 : 75) is satisfied with “a” representing the content (mass%) of the adipic acid and “c” representing the content (mass%) of the succinic acid.

5 11. A soldering paste containing soldering powder and a flux,  
said flux is constituted of the flux of any of claims 1 through 10 and  
said soldering powder is mixed with said flux.

12. The soldering paste of claim 11, wherein:

10 said soldering powder contains at least one of Sn, Cu, Ag, Sb, Pb,  
In, Zn and Bi.

13. An electronic component device comprising at least one  
electronic component, a component mounting board and a soldering flux,  
wherein:

15 said electronic component is soldered onto said component  
mounting board; and

said soldering flux is constituted of the flux of any of claims 1  
through 10 and is present between said electronic component and said  
component mounting board to bond said electronic component and said  
component mounting board to each other.

20 14. An electronic circuit module comprising a semiconductor chip,  
a chip mounting board and a soldering flux, wherein:

said semiconductor chip having at least one semiconductor element  
is soldered onto said chip mounting board; and

25 said soldering flux is constituted of the flux of any of claims 1  
through 10 and is present between said semiconductor chip and said chip  
mounting board to bond said semiconductor chip and said chip mounting  
board to each other.

15. An electronic circuit apparatus comprising an electronic circuit module, a motherboard and a soldering flux, wherein:

said electronic circuit module is soldered onto said motherboard;  
and

5        said soldering flux is constituted of the flux of any of claims 1 through 10 and is present between said electronic circuit module and said motherboard to bond said electronic circuit module and said motherboard to each other.

16. The electronic circuit apparatus of claim 15, wherein:

10        said electronic circuit module is the electronic circuit module of claim 14.

17. A soldering method implemented by using the flux of any of claims 1 through 10.

15        18. A soldering method implemented by using the soldering paste of any one of claims 11 and 12.

19. The method of any one of claims 17 and 18, implemented to solder an electronic component, an electronic circuit module or a semiconductor chip onto a board.

20. The method of claim 19, having;

20        a step in that a soldering process is executed at one surface of said board by using said soldering paste; and

a subsequent step in that an electronic component is soldered onto another surface of said board with a solder different from said soldering paste.

25

## ABSTRACT OF THE DISCLOSURE

The present invention relates to a soldering flux, a soldering paste and a soldering method that achieve a sufficient degree of bonding strength even when components are mounted at a higher density, components are further miniaturized or components are mounted with a smaller pitch. A flux 3 contains an adhesive resin and a hardening agent. This flux or a soldering paste containing the flux is applied onto a component mounting board 1, and then, an electronic component 4 is mounted and soldered onto the component mounting board 1.

Reference: FIG. 1

FIG. 1a

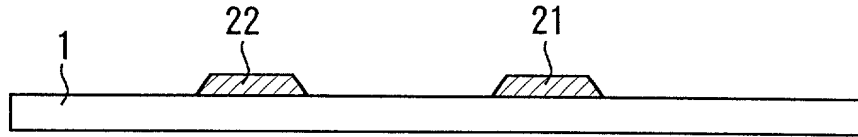


FIG. 1b

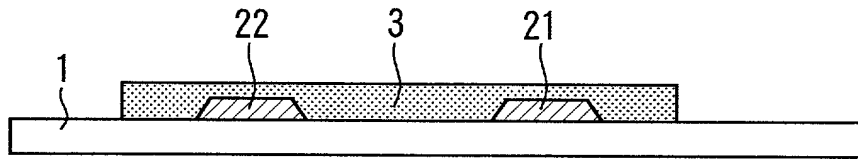


FIG. 1c

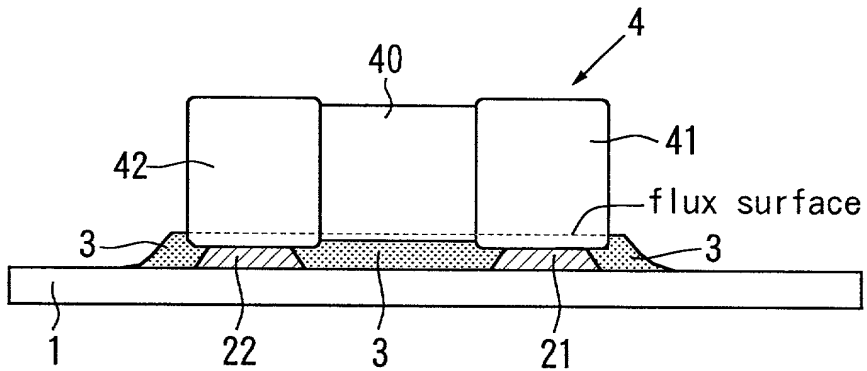
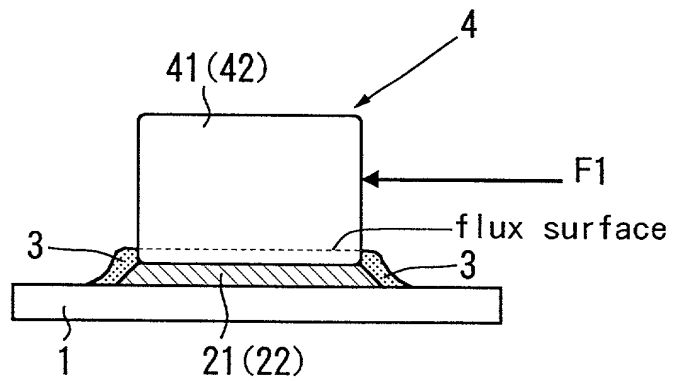


FIG. 2



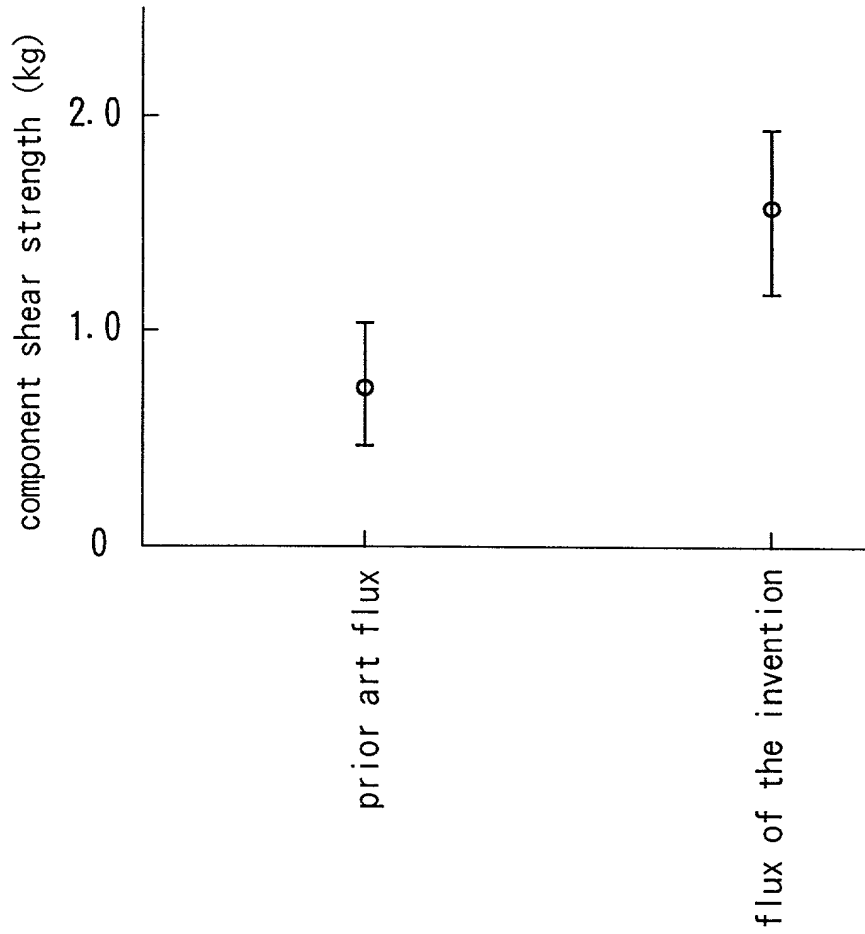


FIG. 3



FIG. 4a

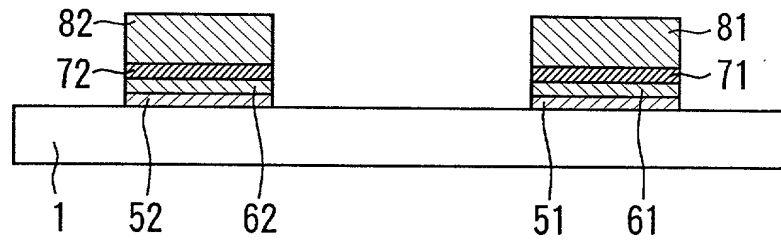


FIG. 4b

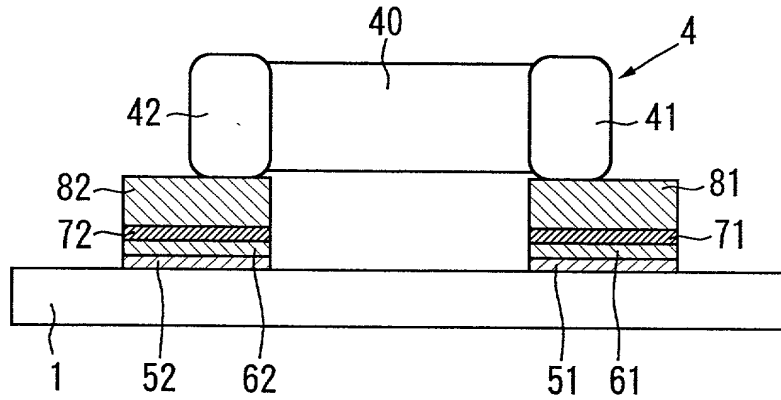


FIG. 4c

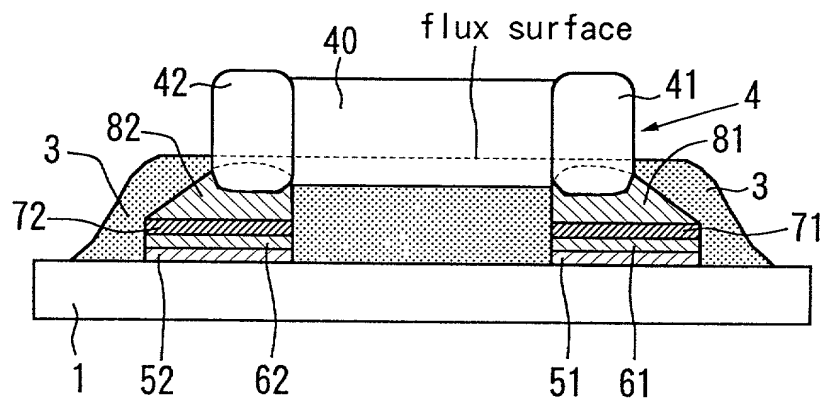


FIG. 5a

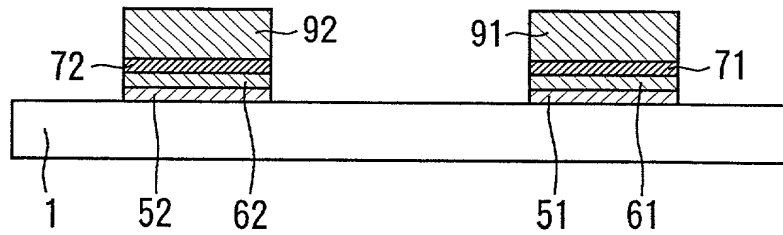


FIG. 5b

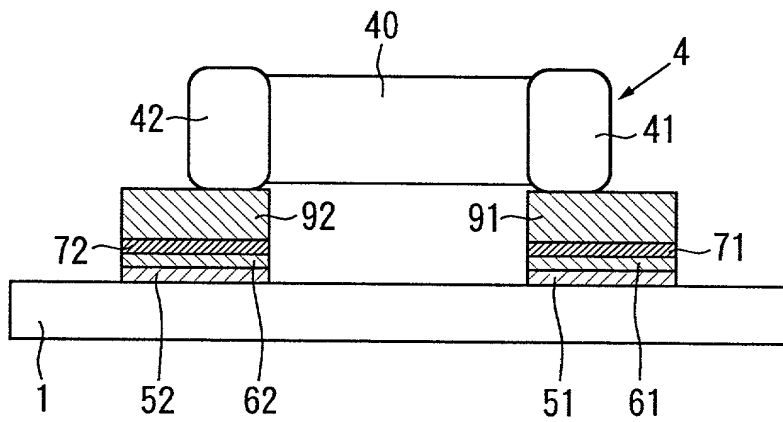
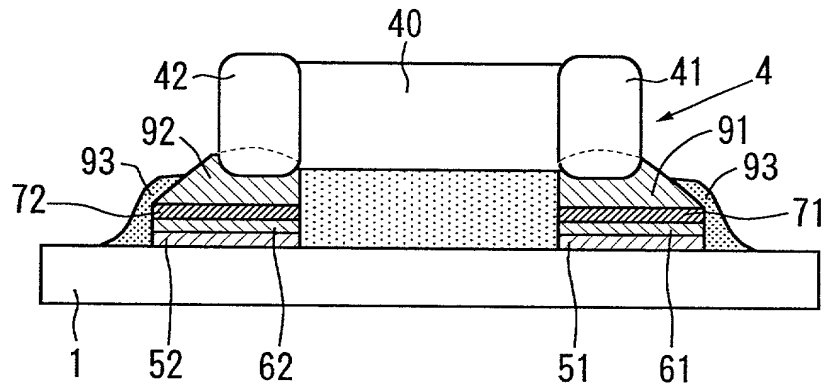


FIG. 5c



PRIOR ART

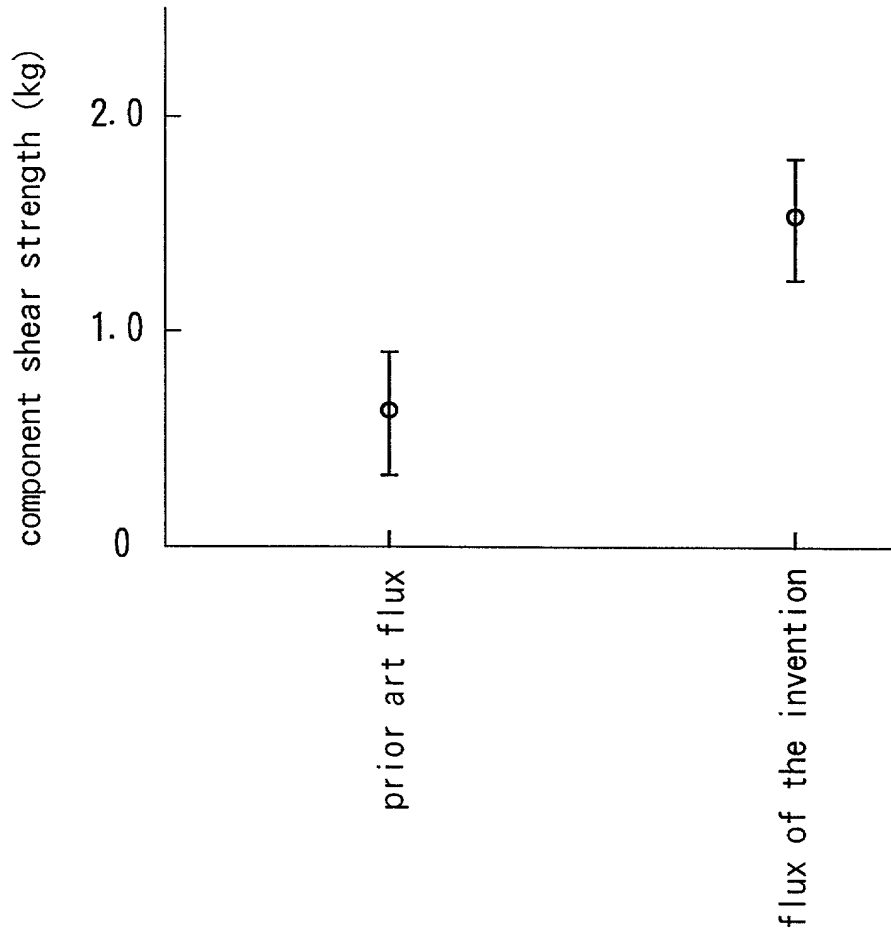


FIG. 6

FIG. 7a

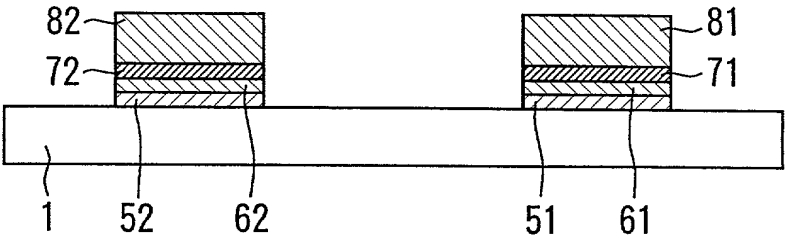


FIG. 7b

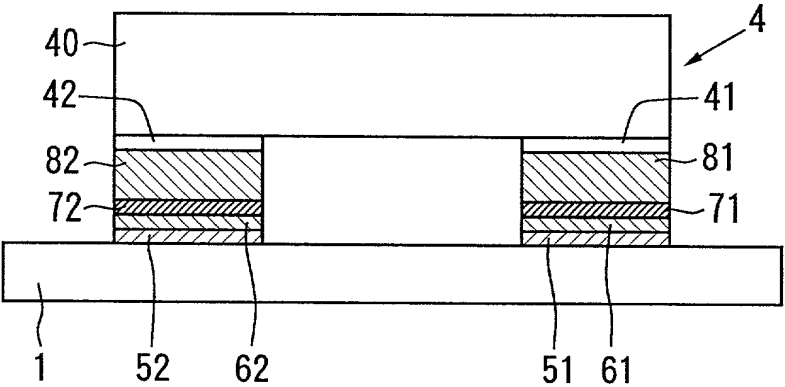
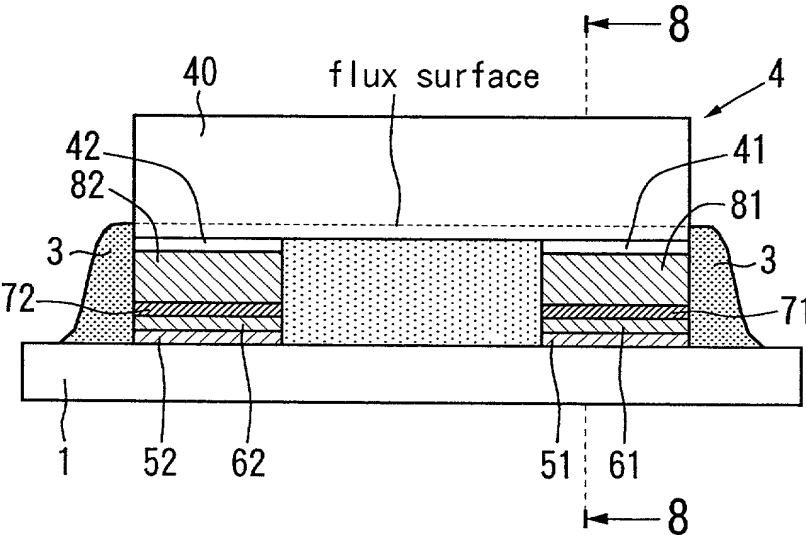


FIG. 7c



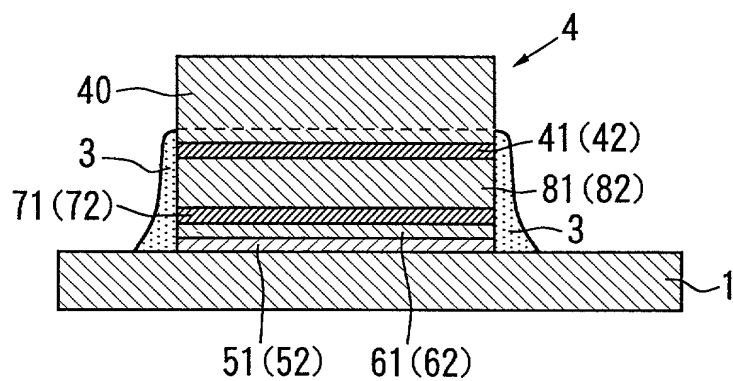


FIG. 8

FIG. 9a

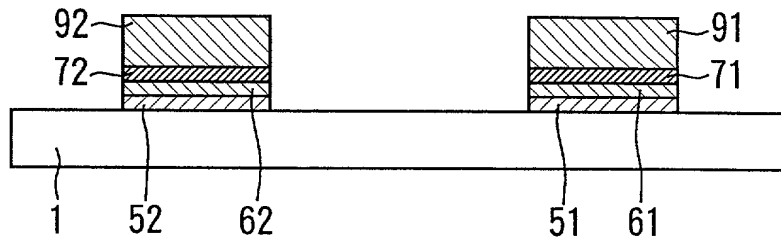


FIG. 9b

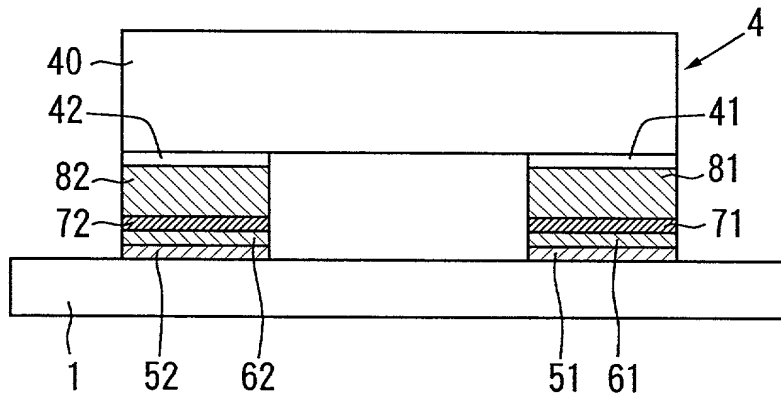
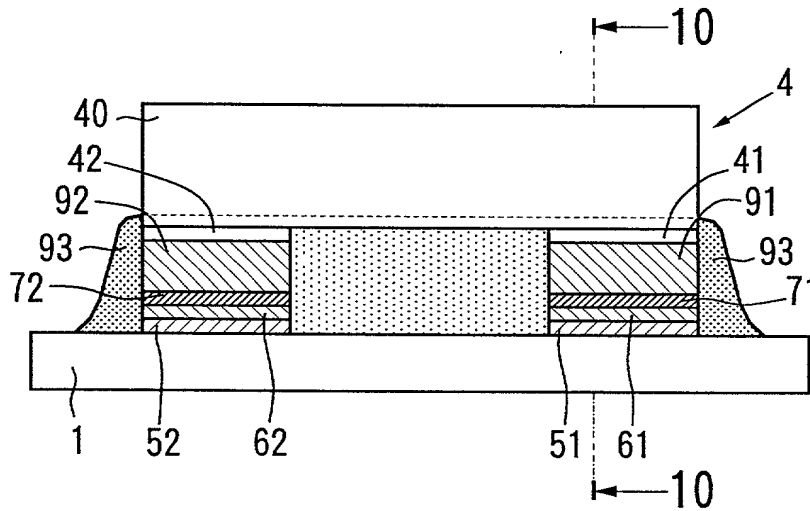


FIG. 9c



PRIOR ART

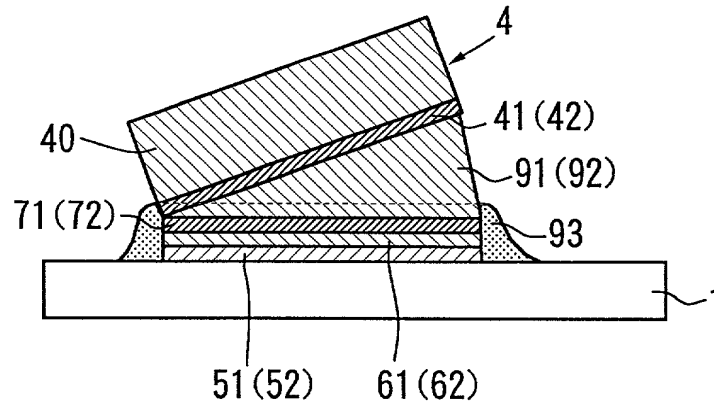


FIG. 10

PRIOR ART

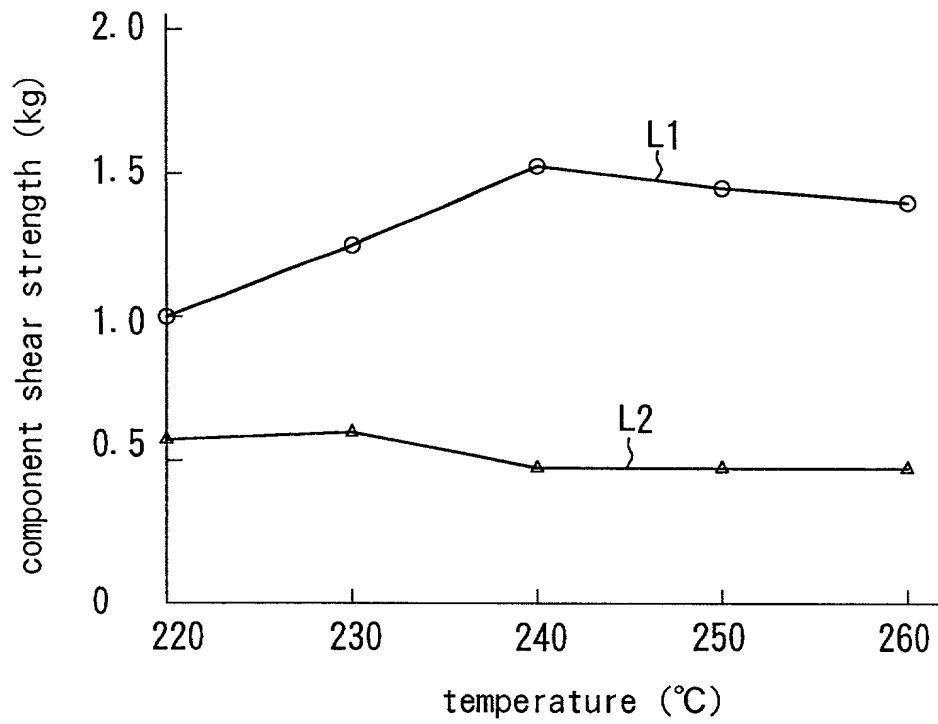


FIG. 11



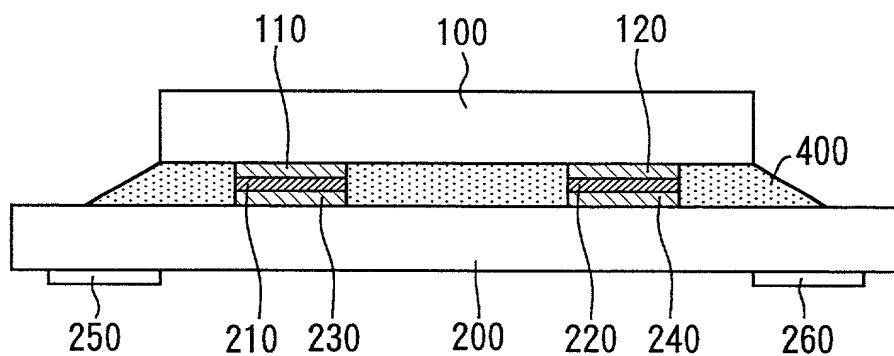


FIG. 12

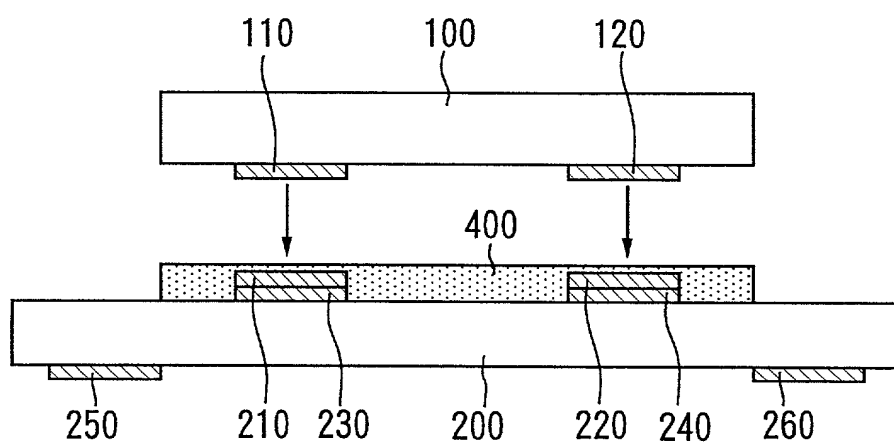


FIG. 13

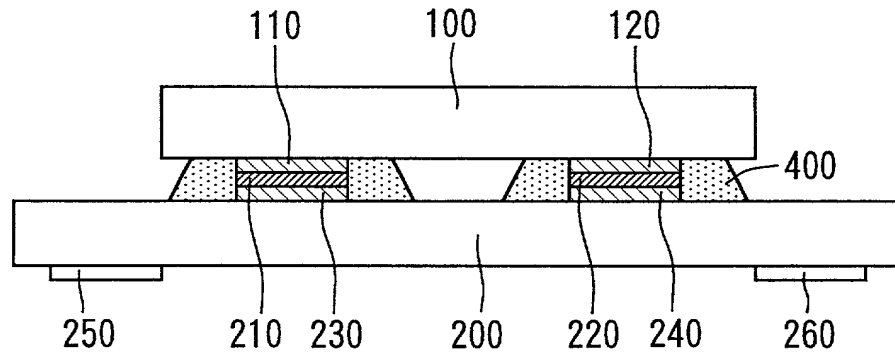


FIG. 14

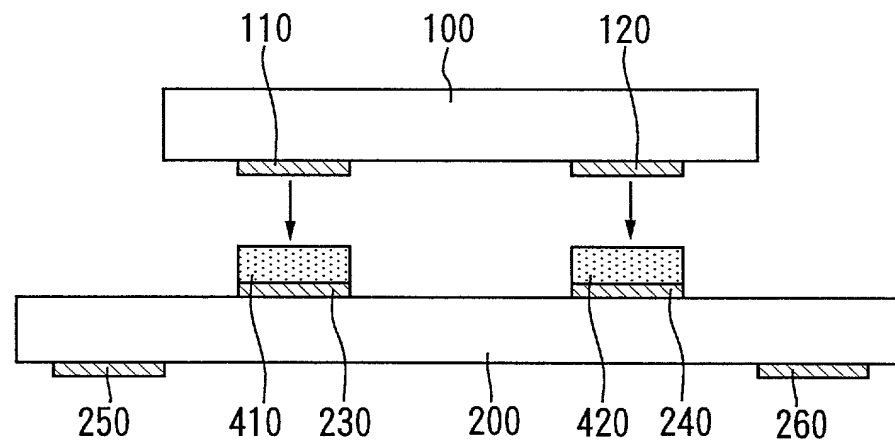


FIG. 15

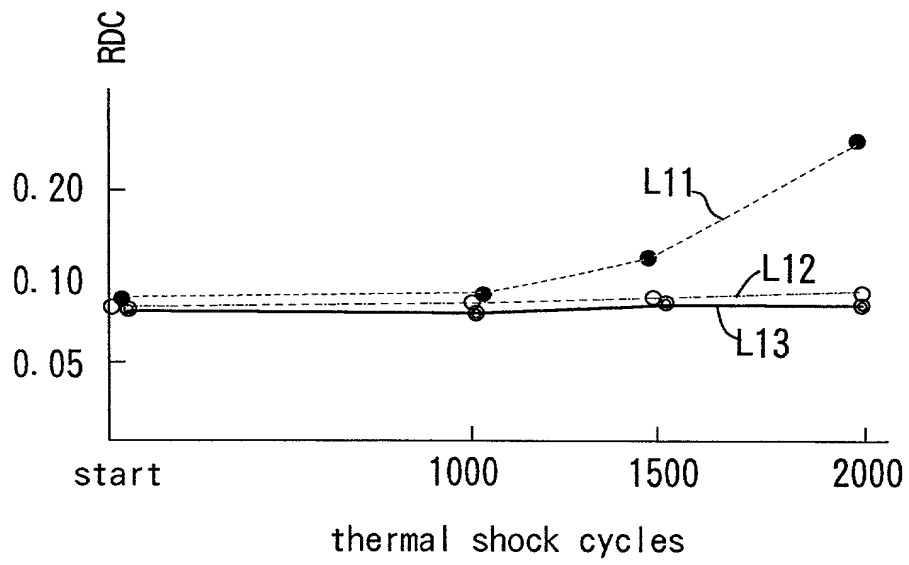


FIG. 16



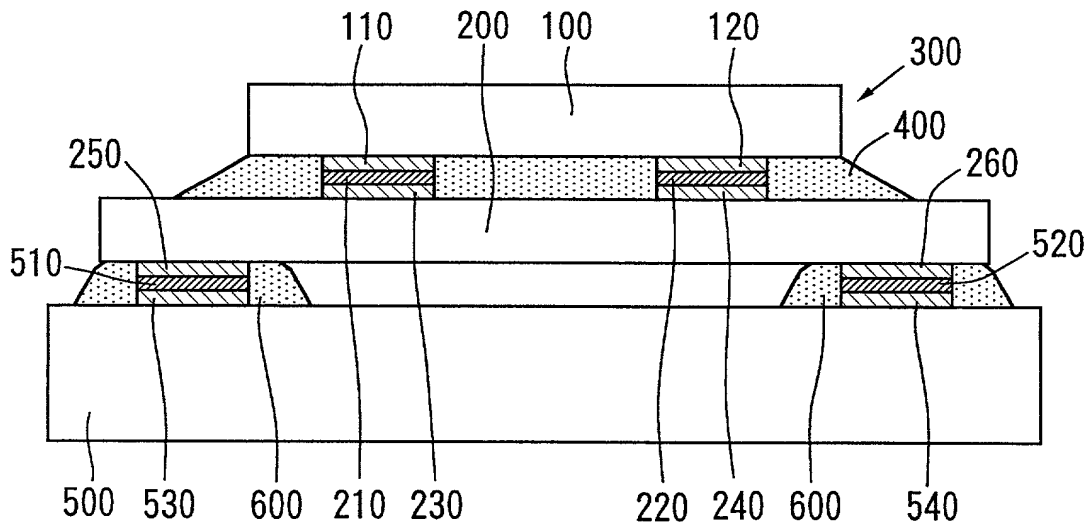


FIG. 19

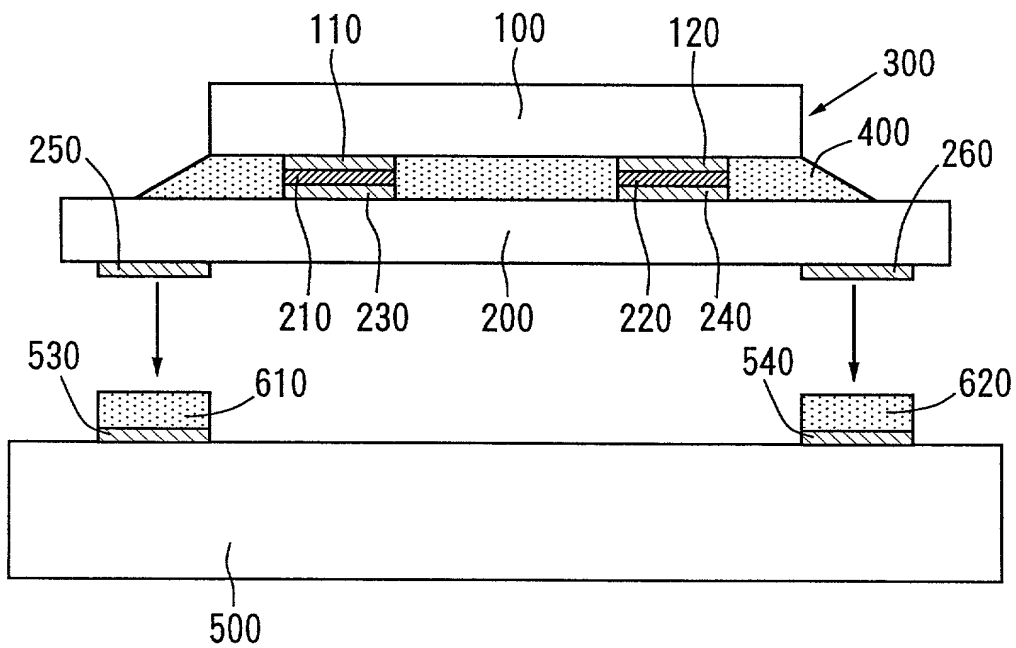


FIG. 20

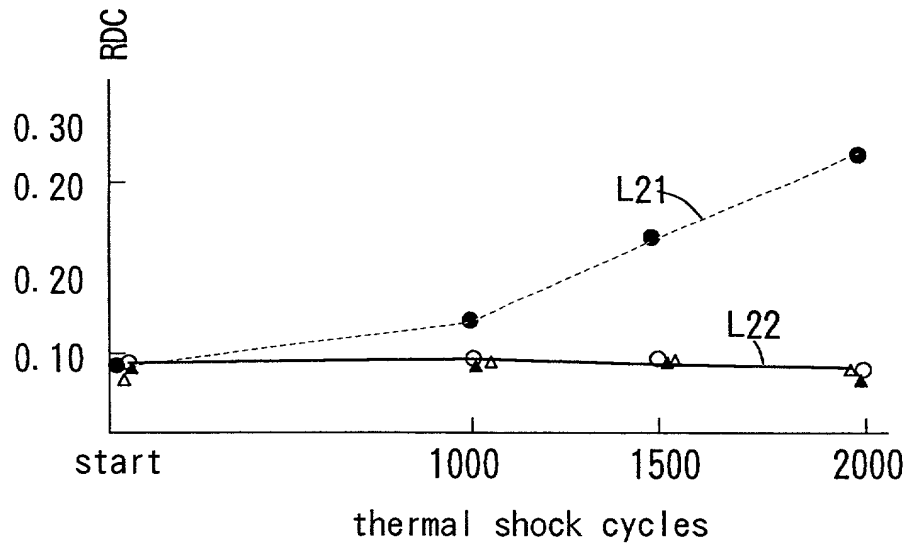


FIG. 21

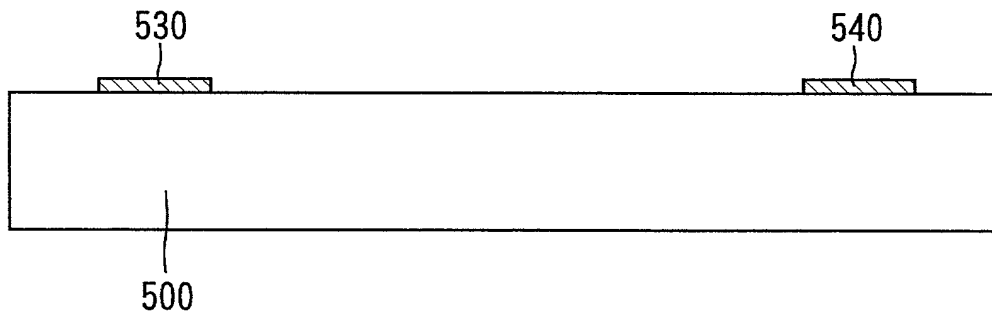


FIG. 22

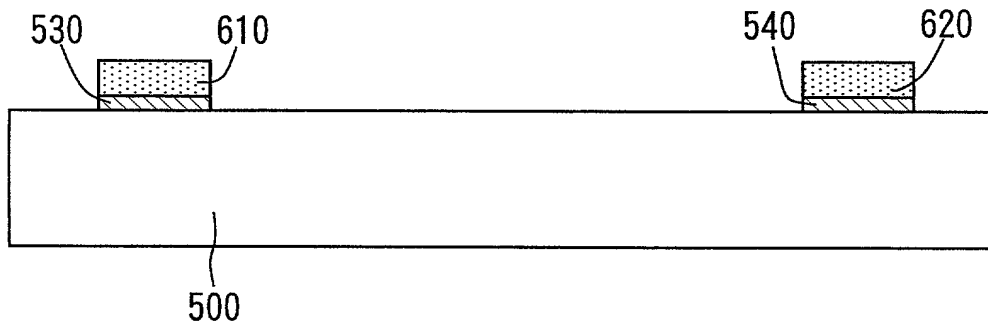


FIG. 23

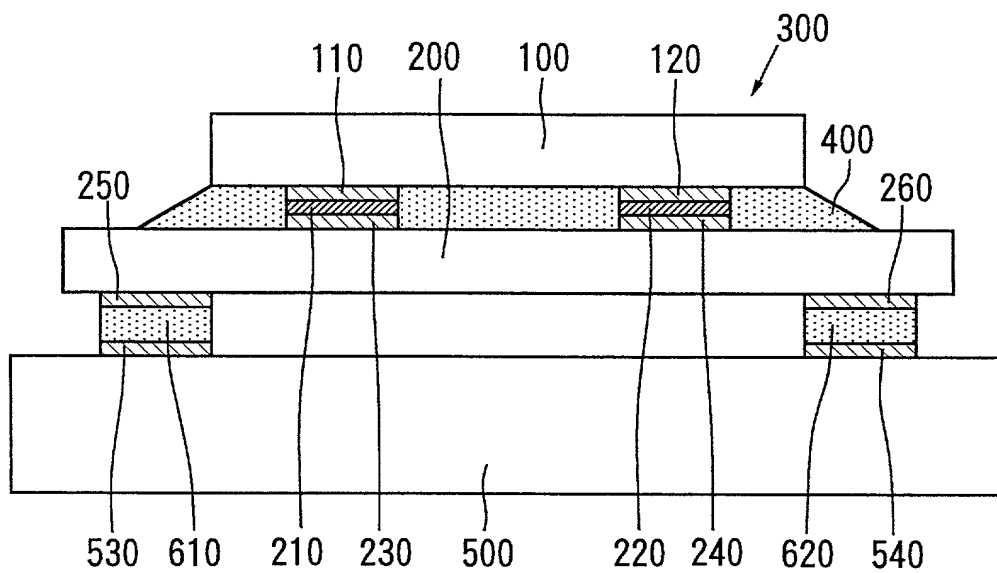


FIG. 24

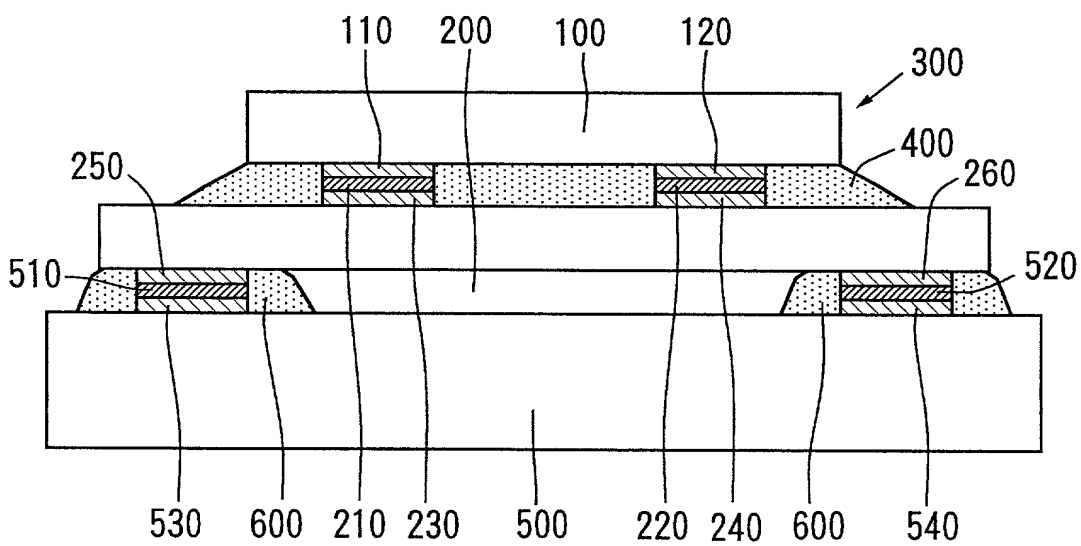


FIG. 25



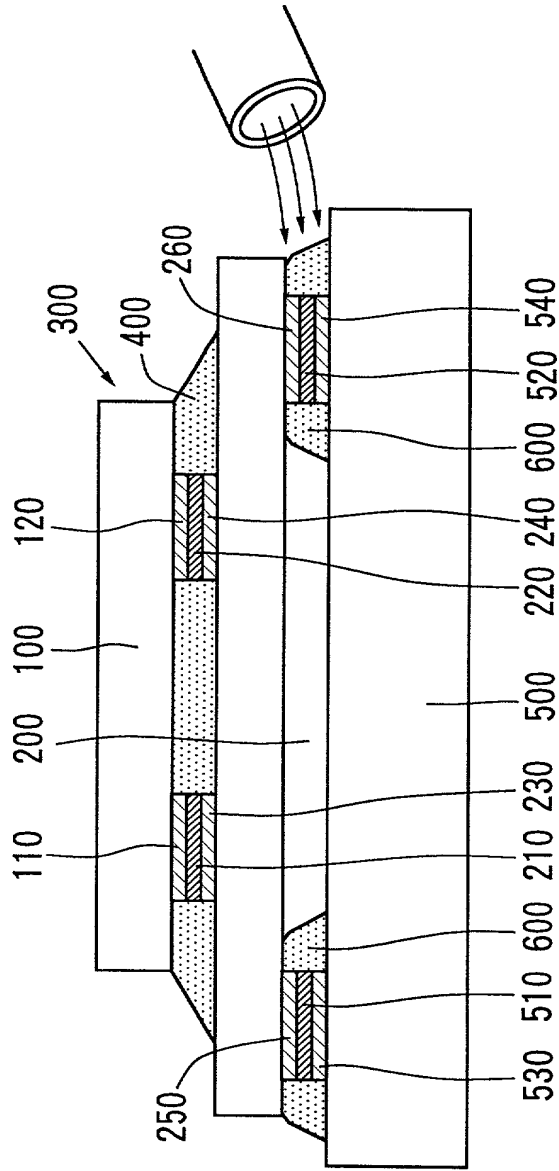


FIG. 26

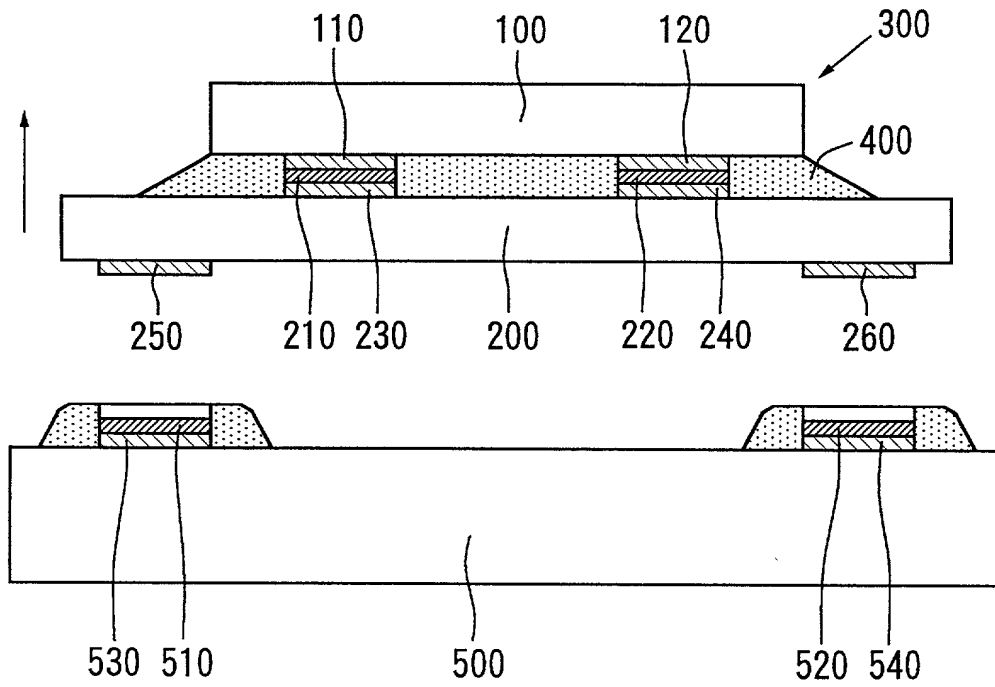
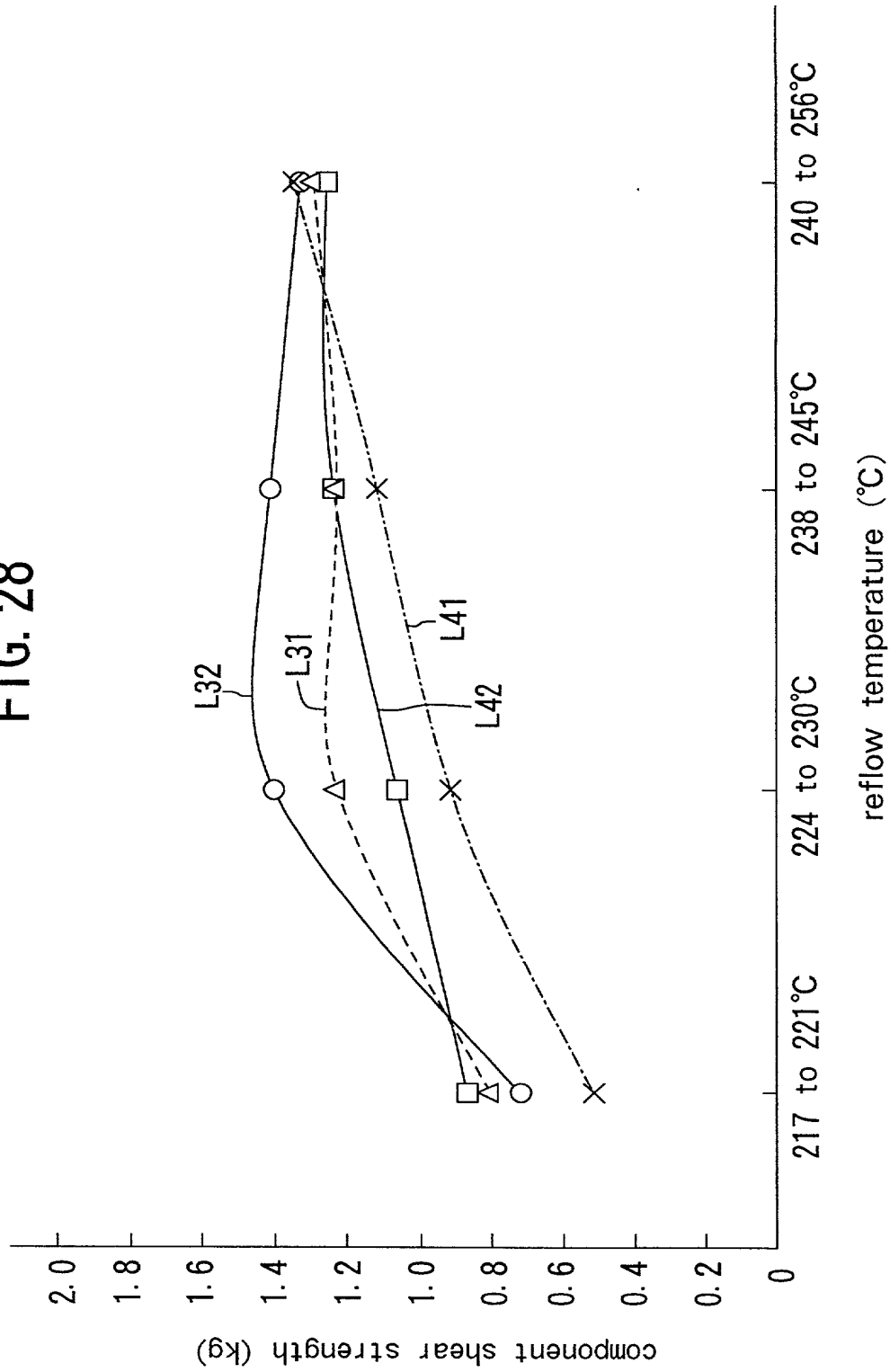


FIG. 27

FIG. 28



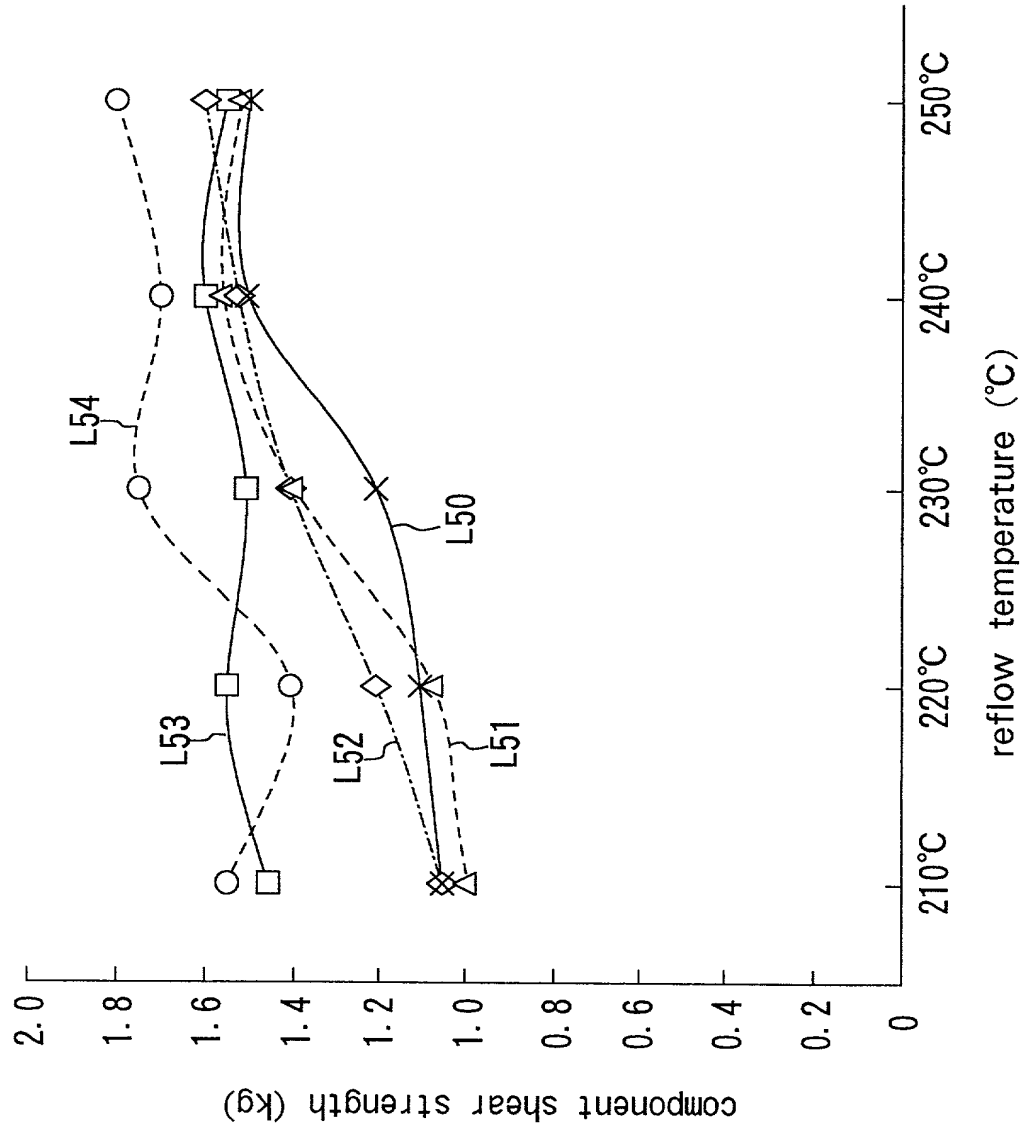


FIG. 29

**THE FOLLOWING IS THE ENGLISH TRANSLATION OF THE  
ARTICLE 34 AMENDED SHEETS (pages 54-56)**

What is claimed is:

1. (Amended) A soldering flux containing an adhesive resin and a hardening agent,

said hardening agent contains at least two types of carboxylic acids having melting points different from each other.

2. The flux of claim 1, assuming either a liquid form or a paste form.

3. The flux of either claim 1 or claim 2 wherein:  
said adhesive resin contains a thermo-setting resin.

4. The flux of claim 3, wherein:  
said thermo-setting resin is constituted of at least one of an epoxy resin, a phenol resin, a polyimide resin, a silicon resin, a modified resin and an acrylic resin.

5. The flux of any of claims 1 through 4, wherein:  
said hardening agent contains a carboxylic acid.

6. (Deleted)

7. (Amended) The flux of any of claims 1 through 5, wherein:  
one of said two carboxylic acids is adipic acid and the other carboxylic acid is pimelic acid.

8. The flux of claim 7, wherein:  
 $a : b = (85 : 15)$  to  $(95 : 5)$  is satisfied with "a" representing the content (mass%) of the adipic acid and "b" representing the content (mass%) of the pimelic acid.

9. (Amended) The flux of any of claims 1 through 5, wherein:  
one of said two carboxylic acids is adipic acid and the other carboxylic acid is succinic acid.

10. The flux of claim 9, wherein:

a : c = (95 : 5) to (25 : 75) is satisfied with “a” representing the content (mass%) of the adipic acid and “c” representing the content (mass%) of the succinic acid.

5 11. (Amended) A soldering paste containing soldering powder and a flux,

said flux is constituted of the flux of any of claims 1 through 5 and 7 through 10 and said soldering powder is mixed with said flux.

12. The soldering paste of claim 11, wherein:

10 said soldering powder contains at least one of Sn, Cu, Ag, Sb, Pb, In, Zn and Bi.

13. (Amended) An electronic component device comprising at least one electronic component, a component mounting board and a soldering flux, wherein:

15 said electronic component is soldered onto said component mounting board; and

said soldering flux is constituted of the flux of any of claims 1 through 5 and 7 through 10 and is present between said electronic component and said component mounting board to bond said electronic component and said component mounting board to each other.

20 14. (Amended) An electronic circuit module comprising a semiconductor chip, a chip mounting board and a soldering flux, wherein:

said semiconductor chip having at least one semiconductor element is soldered onto said chip mounting board; and

25 said soldering flux is constituted of the flux of any of claims 1 through 5 and 7 through 10 and is present between said semiconductor chip and said chip mounting board to bond said semiconductor chip and said chip mounting board to each other.

15. (Amended) An electronic circuit apparatus comprising an electronic circuit module, a motherboard and a soldering flux, wherein:

said electronic circuit module is soldered onto said motherboard;  
and

5        said soldering flux is constituted of the flux of any of claims 1 through 5 and 7 through 10 and is present between said electronic circuit module and said motherboard to bond said electronic circuit module and said motherboard to each other.

16. The electronic circuit apparatus of claim 15, wherein:

10        said electronic circuit module is the electronic circuit module of claim 14.

17. (Amended) A soldering method implemented by using the flux of any of claims 1 through 5 and 7 through 10.

18. A soldering method implemented by using the soldering paste  
15        of any one of claims 11 and 12.

19. The method of any one of claims 17 and 18, implemented to solder an electronic component, an electronic circuit module or a semiconductor chip onto a board.

20. The method of claim 19, having;

20        a step in that a soldering process is executed at one surface of said board by using said soldering paste; and

a subsequent step in that an electronic component is soldered onto another surface of said board with a solder different from said soldering paste.



## Declaration and Power of Attorney For Patent Application

## 特許出願宣言書及び委任状

## Japanese Language Declaration

## 日本語宣言書

下記の氏名の発明者として、私は以下の通り宣言します。

As a below named inventor, I hereby declare that:

私の住所、私書箱、国籍は下記の私の氏名の後に記載された通りです。

My residence, post office address and citizenship are as stated next to my name.

下記の名称の発明に関して請求範囲に記載され、特許出願している発明内容について、私が最初かつ唯一の発明者（下記の氏名が一つの場合）もしくは最初かつ共同発明者（下記の名称が複数の場合）であると信じています。

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled.

はんだ付け用フラックス、はんだペースト及び

"SOLDERING FLUX, SOLDER PASTE AND METHOD OF

はんだ付け方法

SOLDERING"

上記発明の明細書は、

the specification of which

☒ 本書に添付されています。

☒ is attached hereto.

☒ 2000年10月5日に提出され、米国出願番号または特許協定条約国際出願番号をPCT/JP00/06957とし、

☒ was filed on October 5, 2000

as United States Application Number or

PCT International Application Number

PCT/JP00/06957 and was amended on

July 5, 2001 (if applicable).

(該当する場合) 2001年7月5日に訂正されました。

私は、特許請求範囲を含む上記訂正後の明細書を検討し、内容を理解していることをここに表明します。

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

私は、連邦規則法典第37編第1条56項に定義されるとおり、特許資格の有無について重要な情報を開示する義務があることを認めます。

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

**Japanese Language Declaration**  
(日本語宣言書)

私は、米国法典第35編119条 (a) - (d) 項又は365条 (b) 項に基づき下記の、米国以外の国の少なくとも一カ国を指定している特許協力条約365 (a) 項に基づく国際出願、又は外国での特許出願もしくは発明者証の出願についての外国優先権をここに主張するとともに、優先権を主張している、本出願の前に出願された特許または発明者証の外国出願を以下に、枠内をマークすることで、示しています。

Prior Foreign Application(s)  
外国での先行出願

<u>11-284859</u>	<u>Japan</u>
(Number)	(Country)
(番号)	(国名)
<u>2000-083001</u>	<u>Japan</u>
(Number)	(Country)
(番号)	(国名)

私は、第35編米国法典119条 (e) 項に基づいて下記の米国特許出願規定に記載された権利をここに主張いたします。

<u>(Application No.)</u>	<u>(Filing Date)</u>
(出願番号)	(出願日)

私は、下記の米国法典第35編120条に基づいて下記の米国特許出願に記載された権利、又は米国を指定している特許協力条約365条 (c) に基づく権利をここに主張します。また、本出願の各請求範囲の内容が米国法典第35編112条第1項又は特許協力条約で規定された方法で先行する米国特許出願に開示されていない限り、その先行米国出願書提出日以降で本出願書の日本国内または特許協力条約国際提出日までの期間中に入手された、連邦規則法典第37編1条56項で定義された特許資格の有無に関する重要な情報について開示義務があることを認識しています。

<u>(Application No.)</u>	<u>(Filing Date)</u>
(出願番号)	(出願日)

<u>(Application No.)</u>	<u>(Filing Date)</u>
(出願番号)	(出願日)

私は、私自信の知識に基づいて本宣言書中で私が行なう表明が真実であり、かつ私の入手した情報と私の信じているところに基づく表明が全て真実であると信じていること、さらに故意になされた虚偽の表明及びそれと同等の行為は米国法典第18編第1001条に基づき、罰金または拘禁、もしくはその両方により処罰されること、そしてそのような故意による虚偽の声明を行えば、出願した、又は既に許可された特許の有効性が失われることを認識し、よってここに上記のごとく宣誓を致します。

I hereby claim foreign priority under Title 35, United States Code, Section 119 (a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or Section 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed.

<u>05 / October / 1999</u>	<u>Priority Claimed</u>
(Day/Month/Year Filed)	優先権主張
(出願年月日)	<input type="checkbox"/> Yes <input type="checkbox"/> No
	はい いいえ
<u>23 / March / 2000</u>	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No
(Day/Month/Year Filed)	はい いいえ
(出願年月日)	

I hereby claim the benefit under Title 35, United States Code, Section 119(e) of any United States provisional application(s) listed below.

<u>(Application No.)</u>	<u>(Filing Date)</u>
(出願番号)	(出願日)

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s), or Section 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code Section 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of application.

<u>(Status: Patented, Pending, Abandoned)</u>
(現況: 特許許可済、係属中、放棄済)

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

**Japanese Language Declaration**  
(日本語宣言書)

私は、米国法典第35編119条 (a) - (d) 項又は365条 (b) 項に基づき下記の、米国以外の国の少なくとも一カ国を指定している特許協力条約365 (a) 項に基づく国際出願、又は外国での特許出願もしくは発明者証の出願についての外国優先権をここに主張するとともに、優先権を主張している、本出願の前に出願された特許または発明者証の外国出願を以下に、枠内をマークすることで、示しています。

Prior Foreign Application(s)  
外国での先行出願

<u>2000-218046</u>	<u>Japan</u>
(Number)	(Country)
(番号)	(国名)
<hr/>	<hr/>
(Number)	(Country)
(番号)	(国名)

私は、第35編米国法典119条 (e) 項に基づいて下記の特許出願に記載された権利をここに主張いたします。

<u>(Application No.)</u>	<u>(Filing Date)</u>
(出願番号)	(出願日)

私は、下記の米国法典第35編120条に基づいて下記の特許出願に記載された権利、又は米国を指定している特許協力条約365条 (c) に基づく権利をここに主張します。また、本出願の各請求範囲の内容が米国法典第35編112条第1項又は特許協力条約で規定された方法で先行する米国特許出願に開示されていない限り、その先行米国出願書提出日以降で本出願書の日本国内または特許協力条約国際提出日までの期間中に入手された、連邦規則法典第37編1条56項で定義された特許資格の有無に関する重要な情報について開示義務があることを認識しています。

<u>(Application No.)</u>	<u>(Filing Date)</u>
(出願番号)	(出願日)

<u>(Application No.)</u>	<u>(Filing Date)</u>
(出願番号)	(出願日)

私は、私自信の知識に基づいて本宣言書で私が行なう表明が真実であり、かつ私の入手した情報と私の信じているところに基づく表明が全て真実であると信じていること、さらに故意になされた虚偽の表明及びそれと同等の行為は米国法典第18編第1001条に基づき、罰金または拘禁、もしくはその両方により処罰されること、そしてそのような故意による虚偽の声明を行えば、出願した、又は既に許可された特許の有効性が失われることを認識し、よってここに上記のごとく宣誓を致します。

I hereby claim foreign priority under Title 35, United States Code, Section 119 (a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or Section 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed.

<u>18 / July / 2000</u>	Priority Claimed 優先権主張	
(Day/Month/Year Filed)	<input checked="" type="checkbox"/>	<input type="checkbox"/>
(出願年月日)	Yes はい	No いいえ
<hr/>	<input type="checkbox"/>	<input type="checkbox"/>
(Day/Month/Year Filed)	Yes はい	No いいえ
(出願年月日)		

I hereby claim the benefit under Title 35, United States Code, Section 119(e) of any United States provisional application(s) listed below.

<u>(Application No.)</u>	<u>(Filing Date)</u>
(出願番号)	(出願日)

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s), or Section 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code Section 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of application.

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## Japanese Language Declaration

(日本語宣言書)

委任状：私は下記の発明者として、本出願に関する一切の手続きを米特許商標局に対して遂行する弁理士または代理人として、下記の者を指名いたします。  
(弁護士、または代理人の指名及び登録番号を明記のこと)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: (list name and registration number)



022850

書類送付先

Send Correspondence to:



022850

直接電話連絡先：(名前及び電話番号)

Direct Telephone Calls to: (name and telephone number)

(703) 413-3000

単独発明者または第一の共同発明者の氏名 高谷 稔	Full name of sole or first joint inventor Minoru TAKAYA
発明者の署名 日付	Inventor's signature Date
住所 〒103-8272 日本国東京都中央区日本橋一丁目13番1号 ティーディーケイ株式会社内	Residence c/o TDK Corporation, 1-13-1, Nihonbashi, Chuo-ku, Tokyo, 103-8272, Japan
国籍 日本国	Citizenship Japan
郵便の宛先 〒103-8272 日本国東京都中央区日本橋一丁目13番1号	Post Office Address c/o TDK Corporation,
ティーディーケイ株式会社内	1-13-1, Nihonbashi, Chuo-ku, Tokyo, 103-8272, Japan
第二の共同発明者の氏名 阿部 寿之	Full name of second joint inventor, if any Hisayuki ABE
第二の共同発明者の署名 日付	Second joint inventor's signature Date
住所 〒103-8272 日本国東京都中央区日本橋一丁目13番1号 ティーディーケイ株式会社内	Residence c/o TDK Corporation, 1-13-1, Nihonbashi, Chuo-ku, Tokyo, 103-8272, Japan
国籍 日本国	Citizenship Japan
郵便の宛先 〒103-8272 日本国東京都中央区日本橋一丁目13番1号	Post Office Address c/o TDK Corporation,
ティーディーケイ株式会社内	1-13-1, Nihonbashi, Chuo-ku, Tokyo, 103-8272, Japan

(第三以降の共同発明者についても同様に記載し、署名すること)

(Supply similar information and signature for third and subsequent joint inventors.)